From Communicating Machines to Graphical Choreographies

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Abstract

Graphical choreographies, or global graphs, are general multiparty session specifications featuring expressive constructs such as forking, merging, and joining for representing application-level protocols. Global graphs can be directly translated into modelling notations such as BPMN and UML. This paper presents an algorithm whereby a global graph can be constructed from asynchronous interactions represented by communicating finite-state machines (CFSMs). Our results include: a sound and complete characterisation of a subset of safe CFSMs from which global graphs can be constructed; an algorithm to translate CFSMs to global graphs; a time complexity analysis; and an implementation of our theory, as well as an experimental evaluation.

Keywords: multiparty session types, choreography, communicating finite-state machines, global graphs, theory of regions

1. Introduction

Context: Choreographies, models of interactions among software components from a global point of view, have been advocated as a conceptual and practical tool to tackle the complexity of designing, analysing, and implementing modern applications (see e.g., [3, 12, 13, 27]). As noted in [13], besides yielding a global perspective of the coordination of applications supporting the development and verification of single components, a global specification can also be projected so to obtain the local behaviour of components. The software engineering methodology associated with choreographies is usually a uni-directional (top-down) approach to software development life cycle (SDLC). Such a methodology appeals to industry [1, 4, 18] since it allows developers to check components against the corresponding projections of the choreography. However, choreography-based approaches do not fully support SDLC. For example, the ‘conform direction’ of testable architectures [3] lacks algorithms to obtain global models when modifying local projections.

To address this limitation we propose an algorithm to construct choreographies from a set of behavioural specifications of components interacting through asynchronous message passing. We consider the following two scenarios to motivate the practical applicability of our algorithm:

- Distributed service architectures envisage software as a provision made available (through a public interface that hides implementation details) to be dynamically searched by and composed. The choreography of such systems cannot therefore be designed in advance and has to be established and checked at binding-time to attain automatic composition.
- A frequent problem practitioners have to face is the integration of newly developed software with legacy code. Typically, the latter often do not come with a global specification and changes with time. Therefore, it is difficult to assess how modifications to newly developed components fit within the system.

Relying on a modelling notations used in industry, our algorithm enables a bi-directional (top-down and bottom-up) choreography-driven SDLC: a developer can visualise a global viewpoint; thus, when an unexpected choreography emerges, either existing components or the global specification may be refined. Modified choreographies can be projected again so to be compared with the original projections.

Our approach: We adopt communicating finite-state machines (CFSMs) as suitable behavioural specifications of distributed components from which a choreography can be built. CFSMs are a conceptually simple model, based on asynchronous FIFO message-passing communication, and are well-established for analysing properties of distributed systems. They are also widely used in industry tools and can be seen as end-point specifications.

We define an algorithm that, given a set of CFSMs, yields a choreography expressed as a global graph [20], a graphical model closely related to BPMN 2.0 Choreography, advocated as a suitable notation for services [1]. The system $S_{re}$ in Figure 1 will be the running example to illustrate our approach; $S_{re}$ consists of four CFSMs, each having three buffers to communicate with the other participants, that realise a protocol of a fictive game where:

1. Alice ($A$) sends either $bwin$ to Bob ($B$) or $cwin$ to Carol ($C$) to decide who wins the game. In the former case, $A$ fires the transition $AB!bwin$ whereby the message $bwin$ is put in the FIFO buffer $AB$ from $A$ to $B$, and likewise in the latter case.
2. If $B$ wins (that is the message $bwin$ is on top of the queue $AB$ and $B$ consumes it by taking the transition $AB?bwin$), then he

Figure 1. Communicating System $S_{re}$

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The underlying protocol of $\text{Winner}$; in 2, an able condition, called generalised multiparty compatibility mediate even for the simple scenario in Figure 1. We give a decidability which is generally undecidable \cite{14} or computationally hard, and not immediate even for the simple scenario in Figure 1. We give a decidable condition, called \textit{generalised multiparty compatibility} (GMC) that characterises a set of systems for which the questions above can be decided. Our algorithm can produce a global graph from any set of generalised multiparty compatible CFSMs. The global graph is constructed through a transformation of the CFSMs into a safe Petri net, using the algorithm in \cite{19}. The transformation preserves the original CFSMs, which can be recovered by projecting the global graph. Note worthy, most of the systems we found in the literature enjoy GMC and very few of them do not (cf. \S 5).

**Contributions** To the best of our knowledge, this is the first work to build graphical choreographies from CFSMs and to characterise the set of CFSMs from which such choreographies can be built. Our theory is supported by a tool (which we evaluated against protocols from the literature).

Recently the construction of syntactic (non-graphical) multiparty session types \cite{24} from local specifications has been studied in \cite{21,25} for a less general framework with no support for local concurrency; for instance in Figure 1 C can send message \textit{busy} while concurrently receiving either \textit{cwin} or \textit{close} (similarly A can execute input actions \textit{CA}msg and \textit{BA}sig in parallel). We argue that catering for a general form of local concurrency (which is in fact supported by threads in many programming languages) is crucial for modelling real-world systems.

In \cite{11,12} conditions for communicating systems to be safe are given; however, they do not address the problem of constructing choreographies and consider a form of local concurrency more restrictive than ours due to a \textit{single} receiving buffer per participant. We use two uni-directional queues for each couple of participants so that a component can concurrently communicate with many other components accessing different FIFO queues (as, e.g., supported in the TCP protocol suite).

**Synopsis** \S 2 reviews CFSMs. \S 3 defines generalised multiparty compatibility, analyses its complexity (Proposition 3.1 and Proposition 3.2), and its soundness (Theorem 3.1). \S 3.3 discusses how our condition can be used to suggest amendments to fix non-GMC systems. The construction algorithm, its complexity (Proposition 3.4), and its completeness (Theorem 4.1) are in \S 4. The tool and experimental evaluation are in \S 5. We conclude and discuss future work in \S 6 after discussing more related work in \S 7. The full version of this paper \cite{5} include full proofs of our results and benchmark protocols; our tool is available online \cite{6}.

2. Communicating Finite-State Machines

This section reviews definitions and properties of CFSMs. Throughout the paper we use the following sets and notations. Fix a finite set $\mathcal{P}$ of participants (ranged over by $p, q, r, s, \ldots$) and a finite alphabet $\mathbb{A}$. The set of channels is $C = \{pq, p \in \mathcal{P} \text{ and } p \neq q\}$ while $Act = C \times \{!, ?\} \times \mathbb{A}$ is the set of actions (ranged over by $\ell$), $\mathbb{A}^*$ (resp. $Act^*$, ranged over by $\phi$) is the set of finite words on $\mathbb{A}$ (resp. Act). Also, $\ell = (\ell, \alpha, \beta, \gamma) = (\emptyset, \emptyset)$ denotes the empty word, $\langle \phi \rangle$ denotes the length of $\phi$, and $\langle \phi \rangle^q$ is the concatenation of $\phi$ and $\langle \phi \rangle^q$ (we overload these notations for words over $\mathbb{A}$).

**Definition 2.1 (CFSM).** A communicating finite-state machine is a finite transition system given by a 4-tuple $M = (Q, q_0, \delta, \beta)$ where $\delta$ is a finite set of states, $q_0 \in Q$ is the initial state, and $\beta \subseteq Q \times Act \times Q$ is a set of transitions.

The transitions of a CFSM are labelled by actions; label $a$ represents the sending of message $a$ from machine $a$ to $\alpha$ and, dually, $\alpha a$ represents the reception of $a$ by $\alpha$. We write $L(M) \subseteq Act^*$ for the language on $Act$ accepted by the automaton corresponding to machine $M$ where each state of $M$ is an accepting state. A state $q \in Q$ with no outgoing transition is \textit{final}; $q$ is a sending (resp. receiving) state if all its outgoing transitions are labelled with sending (resp. receiving) actions, and $\delta$ is a \textit{mixed} state otherwise.

A CFSM $M = (Q, q_0, \delta, \beta)$ is \textit{deterministic} if for all states $q \in Q$ and all actions $\ell \in Act$, if $(q, \ell, q') \in \delta$ then $q' = q'$. A CFSM $M$ is \textit{minimal} if there is no machine $M'$ with fewer states and transitions than $M$ such that $L(M) = L(M')$. Hereafter, we only consider deterministic and minimal CFSMs.

\footnote{Sometimes, a CFSM is considered deterministic when $(q, a \ell a', q') \in \delta$ and $(q, a \ell a', q') \in \delta$ then $a = a'$ and $q' = q'$. Here, we follow a different definition \cite{17} in order to represent branching type constructs.
Definition 2.2 (Communicating systems). Given a CFSM $M_p = (Q_p, q_0p, A, δ_p)$ for each $p ∈ P$, the tuple $S = (M_p)_{p ∈ P}$ is a communicating system (CS). A configuration of $S$ is a pair $s = (q; w)$ where $q = (q_p)_{p ∈ P}$ and where $w = (w_p)_{p ∈ P}$, with $w_p ∈ \mathbb{A}^*$, component $q$ is the control state and $q_p ∈ Q_p$ is the local state of machine $M_p$. The initial configuration of $S$ is $s_0 = (q_0; \varepsilon)$ with $q_0 = (q_0p)_{p ∈ P}$.

Hereafter, we fix a machine $M_p = (Q_p, q_0p, A, δ_p)$ for each participant $p ∈ P$ and let $S = (M_p)_{p ∈ P}$ be the corresponding system.

Definition 2.3 (Reachable states and configurations). A configuration $s' = (q'; w')$ is reachable from another configuration $s = (q; w)$ by firing transition $ℓ$, written $s, δ s' ∤ (s → s')$ if the label is immaterial, if there is $a ∈ \mathbb{A}$ such that either:

1. $ℓ = a s r a$ and $(q_ℓ, ℓ, q_ℓ') ∈ δ_a$ and
   (a) $q'_p = q_p$ for all $p ≠ a$, and
   (b) $w'_p = w_p s r a$ and $w'_p q = w_p q a$ for all $p ≠ a$; or
2. $ℓ = a s r a$ and $(q_ℓ, ℓ, q_ℓ') ∈ δ_a$ and
   (a) $q'_p = q_p$ for all $p ≠ a$, and
   (b) $w'_p = w_p s r a$ and $w'_p q = w_p q a$ for all $p ≠ a$.

The reflexive and transitive closure of $→$ is $→^*$, i.e., $s →^* s'$ if there is a sequence of configurations $s = s_0 → s_1 → ... → s_n = s'$.

Condition (1b) in Definition 2.3 puts $a$ on channel $s r a$, while (2b) gets $a$ from channel $s r a$. Note that, for every integer $k$, the set $RS_k(S)$ is finite and computable.

We now recall several definitions about communicating systems $S$ and their configurations $s = (q; w)$. We say that $s$ is a deadlock configuration [17] Def. 12] if $w = \varepsilon$, i.e., there is $r ∈ P$ such that $(q_r, s r a, q_r') ∈ δ_r$, and for every $p ∈ P$, $q_p$ is a receiving or final state, i.e., all the buffers are empty, there is at least one machine waiting for a message, and all the other machines are either in a final or receiving state. Configuration $s$ is an orphan message configuration if all $q_p ∈ q$ are final but $w ≠ \varepsilon$, i.e., there is at least a non-empty buffer and each machine is in a final state. Finally, $s$ is an unspecified reception configuration [17] Def. 12] if there exists $r ∈ P$ such that $q_r$ is a receiving state, and $(q_ℓ, s r a, q_ℓ') ∈ δ_r$ implies $w'_r > 0$ and $w_s r a ≠ a \varepsilon$, i.e., $q_r$ is prevented from receiving any message from any of its buffers.

Definition 2.4 (Safe CS). System $S$ is safe if for each $s ∈ RS(S)$, $s$ is not a deadlock, an orphan message, nor an unspecified reception configuration.

The following definitions are new and instrumental for § 3 where we characterise a subset of safe CS from which a global graph can be constructed. A key point to give our condition for a CS to be safe is to identify sets of concurrent actions. Below, we define an equivalence relation on transitions of a CFSM. Given $q, q' ∈ Q$, let $act(q, q') = \{ (ℓ, q, ℓ, q') ∈ δ | (q, q') ∈ \delta \}$ and define $o < δ × δ$ as the smallest equivalence relations that respectively contain the relations $≤$ and $>$ where

- $(q_1, ℓ, q_2) ∥ (q_1', ℓ, q_2')$ if $ℓ ∉ act(q_1, q_1') = act(q_2, q_2') ≠ ∅$
- $(q_1, ℓ, q_2) ∥ (q_1', ℓ, q_2')$ if $ℓ ∈ act(q_1, q_1') = act(q_2, q_2') ≠ ∅$

where $[(q, ℓ, q')]$ denotes the equivalence class of $(q, ℓ, q')$ wrt $o$.

Intuitively, two transitions are $⇑$-related if they refer to the same action up-to interleaving.

Example 2.1. Consider the CFSM below:

The relations in (1-2) hold since both transitions are interleaved with $s r l b$. The relation in (3) does not hold since the transition between the source of one $(q_0)$ and the source of the other $(q_1)$ passes through $s r l a$ itself. The two transitions in (3) are sequential rather than concurrent. The relation in (4) holds, but the relation in (5) does not because there is $(q_0, s r l b, q_6)$ in the $⇑$-equivalence classes of $(q_0, s r l b, q_2)$ for which the condition does not hold (due to the transition with label $s r l c$).

In Figure 1, $(C_0, AcC\text{\`e}win, C_1) ∩ (C_2, AcC\text{\`e}win, C_4)$ since both transitions represent the same action interleaved with CD busy. In each machine in Figure 1 a set of transitions $(q, ℓ, q')$ with the same label $ℓ$ forms a $⇑$-equivalence class, e.g., in Alice, $(A_k, Ck\text{\`a}msg, A_k)$, $(B_k, Ck\text{\`a}msg, B_k)$ is a $⇑$-equivalence class labelled by $Ck\text{\`a}msg$.

3. CFMSs Characterisation of Global Graphs

3.1 Synchronous transition system

Systems amenable to be transformed into global graphs are identified through their synchronous transition system (cf. Definition 3.2) where nodes consist of a vector of local states and transitions are labelled by elements in the set of events $E = \bigcup_{a \in P} Q_a × \{ (s, r) | s ∈ Q_a \} × \{ a \}$. Intuitively, an event $(q_a, q_s, s, r, a) ∈ E$, written $(q_a, q_s, s → r: a)$ for short, indicates that machines $s$ and $r$ can exchange message $a$ when they are respectively in state $q_s$ and $q_r$. Indexing events with the local states of the machines permits to distinguish two occurrences of the same communication at two different points in a global graph. To single out parallelism at the machine level, we introduce an equivalence relation over events that identifies events whose underlying local transitions are $⇑$-equivalent.

Definition 3.1 ($⇑$-equivalence). The event equivalence is the relation $≡⇑ = [≡]_⇑ \cap [≡]_\subseteq \subseteq E \times E$ where

- $(q_1, q_2, s → r: a) ≡⇑ (q_2', q_1, s → r: a)$ if $\forall (q_1, s r l a, q_1), (q_1', s r l a, q_1) ∈ \delta_r : (q_1, s r l a, q_1) ∥ (q_1', s r l a, q_1')$
- $(q_1, q_2, s → r: a) ≡⇑ (q_1, q_2', s → r: a)$ if $\forall (q_2, s r l a, q_2), (q_2', s r l a, q_2) ∈ \delta_r : (q_2, s r l a, q_2) ∥ (q_2', s r l a, q_2')$

We let $[\varepsilon]$ denote the $⇑$-equivalence class of event $\varepsilon$.

Example 3.1. Consider the communicating system below:

The $⇑$-equivalence classes are $(q_0, q_1, q_3) ∩ (q_4, q_2, q_5)$ and $(q_1, q_2, q_3) ∩ (q_4, q_2, q_5)$.
We have labelled transition system: Its synchronous transition system (cf. Definition 3.2 below) is the differentiating them from other occurrences of communications “vertical” transitions and those on the “horizontal” ones equivalent to the same interaction $a \rightarrow r.a$.

A and we extend the notation on $p$-equivalent, i.e., $(\hat{q}, q, \rightarrow p : b)$ in Definition 3.2 (Synchronous transition system) (i.e., $\hat{q}$ and $q$ are $p$-equivalent), i.e., $(A_1, CA^\text{msg}, A_1) \circ (A_2, CA^\text{msg}, A_2)$, and the underlying transition of $C$ is the same for both events, i.e., $(C_5, CA^\text{msg}, C_0)$.

In our running example (cf. Figure 1), we have $(C_{5, A_2, C} : \rightarrow \text{msg}) \circ (C_5, A_1, C : \rightarrow \text{msg})$ since the underlying transitions of $A$ are $e$-equivalent, i.e., $(A_1, CA^\text{msg}, A_3) \circ (A_2, CA^\text{msg}, A_4)$, and the underlying transition of $C$ is the same for both events, i.e., $(C_5, CA^\text{msg}, C_0)$.

Hereafter, we let $n, n', \ldots$ denote vectors of local states and $n[p]$ denote the state of $p \in P$ in $n$.

Definition 3.2 (Synchronous transition system). Given a system $S = (M_p)_{p \in P}$, let $N \stackrel{\text{def}}{=} \{ \langle q, (q', E) \rangle \in RS_1(S) \}$. Its synchronous transition system $\hat{S} = (N, n_0, E, /\rightarrow)$, where $n_0 = \hat{q}_0$ is the initial state, and $n \rightarrow e \rightarrow n'$ iff $(n, e, n')$ is $E$. We fix a set $\tilde{E}$ of representative elements of each $\rightarrow$-equivalence class (i.e., $\tilde{E} \subseteq E$ and $\forall e \in E \exists ! e' \in \tilde{E}$ if $e \in [e]$ and write $n \rightarrow e' \rightarrow n'$ for $e' \in [e]$ and write $n \rightarrow e' \rightarrow n'$ for $n \rightarrow e \rightarrow n'$ when $e' \in [e] \cap \tilde{E}$. Sequences of events are ranged over by $\pi$.

Definition 3.3 (Projections). The projection of an event $e$ onto participant $p$, denoted by $e|_p$, is defined as follows:

\[
(q_0, q_0, p \rightarrow r : a) |_p \stackrel{\text{def}}{=} \begin{cases} \text{pr} & \text{if } a = p \\ \text{ap} & \text{if } r = p \\ \varepsilon & \text{otherwise} \end{cases}
\]

Projection is defined on sequences of events in the obvious way. The projection of $TS(S) = (N, n_0, E, /\rightarrow)$ is denoted $\hat{S} = (N, \hat{n}_0, \hat{E}, /\rightarrow)$. Essentially, GMC relies on two conditions, (1) representability (cf. Definition 3.4) for each machine, each trace and each choice are sound and complete condition for constructing global graphs. Hereafter, we fix a system $S = (M_p)_{p \in P}$ with $TS(S) = (N, n_0, E, /\rightarrow)$. Essentially, GMC relies on two conditions, (1) representability (cf. Definition 3.4) for each machine, each trace and each choice are sound and complete condition for constructing global graphs.

For a language $L$, $h_d(L)$ returns the first actions of $L$ (if any). $h_d(L) \stackrel{\text{def}}{=} \{ \ell : \exists \phi \in Aro^L : \ell \cdot \phi \in L \}$ $h_d([\varepsilon]) \stackrel{\text{def}}{=} \{ \varepsilon \}$

Given $n \in N$, let $TS(S, n)$ be the transition system $TS(S)$ where the initial state $n_0$ is replaced by $n$. We write $LT(S, n, p)$ for $LT(S, n, p)$ when $TS(S, n)$ is the language obtained by setting the initial node of $TS(S)$ to $n$ and then projecting this new transition system onto $p$.

Definition 3.4 (Representability). System $S$ is representable if
1. \( L(M_p) = LT(S,n,p) \) and 
2. \( \forall q \in Q_p \exists n \in N: n[p] = q \wedge \bigcup_{(q',q) \in E_p} \{ q \} \leq hd(lLT(S,n,p)) \).

for all \( p \in P \).

Condition (1) in Definition 3.3 is needed to ensure that each trace of each machine is represented in \( TS(S) \); while condition (2) is necessary to ensure that every choice in each machine is represented in \( TS(S) \).

**Proposition 3.1.** Given a system \( S = (M_p)_p \in P \), checking whether \( S \) satisfies the representability condition is computable in

\[
\sum_{p \in P} |Q|^{N_1} |Q| \text{ time, with } |N| = \prod_{p \in P} |Q_p|.
\]

In the worst case, the time complexity of checking the representability of \( S \) is exponential. This is solely due to the language equivalence check (condition (1) in Definition 3.3) between each machine and its projection from \( TS(S) \). However, as observed in [13], in practice algorithms for language equivalence behave very efficiently. In addition, we can remove some states from the projection of \( TS(S) \), e.g., those that are on chains of e-transitions only, while preserving its language, thus reducing the exponent \( |N| \).

We give a few auxiliary definitions before formalising the first conditions for GMC, which ensures that each “global choice” is made by exactly one participant and that all the other participants are either made aware of the choice or not involved in/affected by the choice.

**Definition 3.5** (Branching property). System \( S \) has the branching property if for all \( n \in N \) and for all \( e_1 \neq e_2 \in E \) such that \( n \models n_1 \) and \( n \models n_2 \), then we have that

1. either there is \( n' \in N \) such that \( n \models n_1 \) and \( n \models n_2' \), or
2. for each \( (n_1', n_2') \in \prod(n(n,e_1, e_2)) \), letting

\[
L_p \defeq \{ \{ e_1 \} \cup p \mid p \in LT(S,n_1', n_2') \}
\]

with \( i \in \{1, 2\} \) and \( p \in P \), conditions (a), (b), and (c) below hold.

(a) choice-awareness: \( \forall p \in P \) either

- \( i \in L_p \cap L_p \), \( e \in L_{p, q_1} \), \( e \in L_{p, q_2} \),
- \( i \in L_p \cap L_p \), \( e \in L_{p, q_1} \), \( e \in L_{p, q_2} \),
- \( i \in L_p \cap L_p \), \( e \in L_{p, q_1} \), \( e \in L_{p, q_2} \),

(b) unique selection: \( \exists p \in P \) \( L_p \cap L_p = \emptyset \) \( \forall a \in L_{p, q_1} \), \( \forall a \in L_{p, q_2} \),

(c) no race: \( \forall \tau : \tau \in L_p \cap L_p = \emptyset \) \( \forall a \in L_{p, q_1} \), \( \forall a \in L_{p, q_2} \),

Definition 3.5 ensures that every branching either is (1) the concurrent execution of two events; or, for each participant \( p \), there is a unique participant \( q \) that does not terminate before \( n \), then the first actions of \( p \) in two different branches are disjoint; or (2a) \( p \) is not involved in the choice, i.e., the branches merge before \( p \) does any action; (2b) there is a unique participant \( s \) making the decision; and (2c) for each participant \( p \) involved in the choice, there cannot be a race condition between the messages that \( p \) can receive. The no race condition notably ensures that in any (asynchronous) execution of \( S \), if a machine has more than one non-empty buffers, then it can read from them in any order (interleaving is possible). Note that if a machine \( \tau \) receives all its messages from a single sender, then there is a \( \prec \)-relation between all its actions.

In system \( S_{ev} \), case (1) of Definition 3.5 applies to all branching nodes except \( n_0 = (A_0, B_0, C_0, D_0) \) and \( n = (A_0, B_0, C_2, D_1) \), highlighted in Figure 3 for which case (2) applies. For \( e_1 = (A_0, B_0) \rightarrow B: bwin \) and \( e_2 = (A_0, D_0) \rightarrow C: cwin \), we have \( L(n_0, e_1, e_2) = \{ (n_0, e_2) \} \). Hence, \( e_1, e_2 \) holds for \( n_0 \) iff it holds for \( n_0 \). Following (2a), we check that every participant satisfies either (2a) or (2b):

- A executes different (sending) actions in both branches (AB!bwin and AC!cwin),
- B executes different (receiving) actions (AB?bwin and CB?blose),
• C executes different (receiving) actions (AC?cwin and BC?close), hence case (2a) applies to A, B, and C. While case (2a) applies to D since there is a node \( n' = (A_1, B_2, C_3, D_1) \) such that D does not execute any action on either path from \( n \) to \( n' \) (through nodes \( (A_1, B_1, C_2, D_1) \) and \( (A_1, B_0, C_4, D_1) \), respectively). Also, condition (2b) is satisfied since A is the unique sender that executes different actions in both branches e1 and e2.

Condition (2c) is satisfied for B and C due to the existence of dependency chains from AB?bwin to CB?close (and vice versa) and from AC?cwin to BC?close (and vice versa). For instance, the dependency chain B → C: close ∨ C → A: msg ∨ A → C: cwin prevents C to delay the reception of close (sent by B) until she can receive message cwin (sent by A); C must send a message msg (to A) before she can receive the outcome of a new round of the game.

Finally, note that \( ln(n_0, e_1, e_2) \) ensures that checking the branching between e1 and e2 at node \( n_0 \) is delayed until the interaction C → D: busy does not interfere with the choice. Hence, the behaviours of C and D are checked only once they have exchanged the busy message.

**Proposition 3.2.** Given a system \( S = (M_p)_{p \in P} \), checking whether S satisfies the branching property is computable in

\[
O \left( \lceil \Delta^2 \rceil \times \lceil \mu \rceil \times \sum_{r \in \rho} \left( \lceil \delta_r^2 \rceil \right) \right) \text{ time.}
\]

Checking the branching property is factorial in the size of \( TS(S) \) because it requires the enumeration of paths of \( TS(S) \) (cf. (2c) of Definition 3.5). We remark that the above is a rather coarse approximation obtained under worst case assumptions oblivious of the typical structure of \( TS(S) \); our experiments show good performances (cf. § 5). Finally, we observe that \( TS(S) \) is generally much smaller than, e.g., the one-bound transition system of S (where each queue may contain at most one message).

**Definition 3.6 (Generalised multiparty compatibility).** A system S is generalised multiparty compatible (GMC) if it is representable and has the branching property.

**Example 3.3.** We show the interplay between the representability and branching conditions by exhibiting unsafe systems satisfying only one of the properties. Consider the following machines:

- **A**
  - AC?c
  - BA?b
  - AB?a
  - AC?c
  - BA?b
  - AB?a

- **B**
  - AC?c
  - BA?b
  - AB?a
  - AC?c
  - BA?b
  - AB?a

(1) System \( S_1 = (A, B, C_1) \) with \( d = d' \) is not safe: whenever the left-hand side branch of A and the right-hand side branch of B are taken in a same execution, \( S_1 \) will reach an orphan message configuration where messages x and y are never consumed. In fact, \( S_1 \) is not GMC because there is a branching node from which B can execute, as first actions, either AB?a or CB?d, and there is no dependency between the reception of a and that of \( d' \) (with \( d = d' \)) in the left-hand side branch, i.e., \( (A \rightarrow B: a \triangleright A \rightarrow C: c' \triangleright C \rightarrow B: d') \). Thus the branching property does not hold.

(2) System \( S_2 = (A, B, C_2) \) with \( d \neq d' \) is not safe: as before, whenever the left-hand side branch of A and the right-hand side branch of B are taken in a same execution this system reaches an orphan message configuration. These two branches are not mutually exclusive since \( C_2 \) can receive \( c' \) then send \( d \). This system is not GMC since there is no node in \( TS(S_2) \) such that actions CB?d and CB?d' are the first actions executed by C. Hence the representability condition does not hold.

(3) System \( S_3 = (A, B, C_3) \) with \( d \neq d' \) is safe and is GMC. In \( S_3 \), the left-hand side branch of A and the right-hand side branch of B are always mutually exclusive, while in \( S_1 \) and \( S_2 \) they are only mutually exclusive in synchronous executions.

We remark that systems \( S_1 \) and \( S_2 \) may be easily changed so that they are "safe" in any k-bounded execution but not safe in a k+1-bounded execution. This may be done by making A and B exchange \( k+1 \) messages consecutively, e.g., by replacing every AB?a (resp. AB?b) transitions in A (resp. B) by a sequence of \( k+1 \) transitions AB?a1 (resp. AB?b1), for \( 1 \leq i \leq k+1 \).

**Theorem 3.1 (Soundness).** If S is GMC, then it is safe (no orphan message, deadlock, and unspecified reception configurations).

Theorem 3.1 says that no (asynchronous) execution of S will result in an orphan message, deadlock or unspecified reception configurations. Relying on representability (every transition and branching in each machine is represented in \( TS(S) \), the proof shows that, for each branching node \( n \), the function \( ln(n, e_1, e_2) \) allows enough branches to be verified against the branching property. Then, it shows that any sent message is eventually received and that a machine in a receiving state eventually receives a message it expected, by Definition 3.5.

**Example 3.4.** The unsafe system below has the branching property and validates condition (1) of Definition 3.4 but not condition (2).

This system can reach an orphan message configuration, where messages x and y are never received.

This example illustrates the importance of condition (2) of Definition 3.4 to ensure safety. In the TS of this system (isomorphic to machine A), the branches corresponding to DA?y and DA?x of machine D are not checked against each other for the branching property.

### 3.3 Amending communicating systems

When a system is not GMC, our algorithm can be used to suggest different ways of transforming it, so to validate the condition. By Definition 3.6 we first note:

**Proposition 3.3.** If S satisfies all but (1) in Definition 3.4 then the system consisting of the (minimised) projections of \( TS(S) \) is GMC.

This means that, in such a case, a new safe system may be automatically obtained from the projections of \( TS \). For instance, system \( S_2 \) in Example 3.3 is not GMC because (1) in Definition 3.4 does...
not hold. However, the system corresponding to the projections of $TS(S_2)$ is exactly system $S_3$, which is GMC.

In case the projections of $TS(S)$ do not provide a viable alternative, then the language equivalence check allows to highlight which transitions (or paths) of each machine are not represented in $TS(S)$. Similarly, local states and transitions violating it can be singled out, according to condition (2) in Definition 3.4. For instance, in Example 3.4, we can highlight all transitions over $x$ and $y$, as well as the states where they are enabled.

When the branching property (Definition 3.5) is violated, then our analysis permits to give precise information on where the problem occurs. First, we can give the vector of local states and the two branching events for which the problem occurs as well as a witnessing execution that leads to the offending configuration.

• If the choice-awareness condition (2a) is violated, then we can list the machines for which the condition is not satisfied. If a machine has a first same receiving action in both branches, then it may be corrected by simply renaming some messages. These renamings can be automatically suggested while checking for the branching property. If the condition fails because a machine terminates in one branch but not in the other, then we can suggest to add a new label and a transition to the final state in the terminated branch; as well as a dual transition in a sending machine.

• If condition (2b) is violated, we can highlight the set of machines sending messages at this branching node. A solution may be found by identifying the genuine selecting machine and add communications from this machine to the others.

• If condition (2c) is violated, then we can highlight, for each machine violating the condition, on which messages a race condition may occur; and suggest to add an acknowledgement message between the two corresponding actions.

Note that since CFMSs are specification or abstraction of programs, it is generally not desirable to automatically repair non-GMC systems. Indeed, some corrections may not be reflected easily in the corresponding implementations. Indeed, some corrections may not be reflected easily in the corresponding implementations. Indeed, some corrections may not be reflected easily in the corresponding implementations.

4. Building Global Graphs

In § 5, we construct the synchronous transition system $TS(S)$ of a communicating system $S$, and check whether it is GMC. We now describe the construction algorithm and its properties; Figure 4 summarises the work-flow of the transformations.

The algorithm to construct a global graph $G$ from a synchronous transition system $TS(S)$ consists of the following steps:

1. we apply the algorithm of Cortadella et al. [19] to derive a Petri net $N$ from $TS(S)$;
2. we transform $N$ so that its initial marking consists of exactly one place;
3. we join transitions whenever possible, so to make explicit join and fork points of the work-flow;
4. we transform the net of (3) into a pre-global graph; finally, we “clean-up” the pre-global graph of unnecessary vertices so to obtain a global graph.

For the sake of the presentation and because the transformations are rather mechanical, we explain them through our running example. The formal definitions of the transformations and additional results are given in Appendix B.

For (1), it is enough for the reader to know that the algorithm in Cortadella et al. [19] is based on the theory of regions [8] and transforms a transition system into a safe and extended free-choice labelled Petri net, whose reachability graph is bisimilar to the original transition system. Basically, this algorithm transforms events of $TS(S)$ into transitions of $N$ while the places are built out of regions, i.e., sets of states having a uniform behaviour with respect to events.

We assume in this section that each $TS(S)$ is self-loop free, i.e., $\forall n, n' \in N : n \rightarrow n' \Rightarrow n = n'$. The algorithm of [19] is applicable on a self-loop free $TS(S)$, since every event $e \in \hat{E}$ has an occurrence in $TS(S)$ by construction and every state $n$ reachable from $n_0$, as stated in Lemma 4.1, below. The Petri net obtained from $TS(S_{ev})$ in Figure 3 is given in Figure 5 (left).

Lemma 4.1. If $S$ is GMC and $TS(S) = (N, n_0, \hat{E}, \Rightarrow)$, then $\forall n \in N : n_0 \Rightarrow^* n$.

In step (2), we transform a Petri net obtained from Cortadella’s algorithm into a Petri net whose initial marking consists of exactly one place. This allows us to construct a global graph that has a unique starting point. In our running example, the Petri net on the left of Figure 5 is transformed by adding a fresh place ($p_0$), initially marked, and a fresh (silent) transition ($t_0$) connected to places $p_1$ and $p_2$ (this simple transformation is not illustrated in Figure 5).

In step (3), a transformation ensures that parallel gates are used “as much as possible” in the graph (instead of mixing choice and parallel gates). In fact, the transformation joins sets of places that have the same preset or postset to minimise the number of choice gates. The Petri net in the middle of Figure 5 is the net obtained from the left-hand side net after applying step (2) and (3). In the second transformation, we add (i) $t_1$ and $p_{11}$ so to join $p_1$ and $p_2$ which have the same preset, i.e., $t_0$ and the transition with label ($A_0, D_1, A \rightarrow D_0$: free); and (ii) we add $t_2$ and $p_{10}$ so to join $p_3$ and $p_6$ which have the same preset, i.e., the transitions with labels ($C_1, B_0, C \rightarrow B$: close) and ($B_1, C_0, B \rightarrow C$: close). Both $t_1$ and $t_2$ are silent transitions.

Let $\approx$ be the weak bisimilarity relation on reachability graphs (i.e., $\approx$ is the bisimilarity up-to silent transitions, cf. Appendix A).

Lemma 4.2. Let $N_1$ be the Petri net obtained after step (1), let $N_2$ (resp. $N_3$) be obtained by applying step (2) (resp. (3)) to $N_1$ (resp. $N_2$). If $T_i$ is the reachability graph of $N_i$ (for $i = 1, 2, 3$) then $T_1 \approx T_2 \approx T_3$.

We now define global graphs (a superclass of the generalised global types of [20] that allows each gate to be connected to more than two predecessors or successors).

Definition 4.1 (Global graph). A global graph (over $P$ and $A$) is a labelled graph $(V, A, A)$ with set of vertexes $V$, set of edges $\Lambda \subseteq V \times V$, and labelling function $\Lambda$ from $V$ to $\{\circ, \bullet, \bigcirc\} \cup \{s \rightarrow a | a \in P \land a \in A\}$ such that, $\Lambda^{-1} (\bigcirc)$ is a singleton, and for each $v \in V$, if $\Lambda (v)$ is of the form $s \rightarrow a : t$ then $v$ has unique incoming

2In $TS(S)$, if an event $e$ self-loops, then any transition labelled by $e$ is a self-loop. Hence, we can easily lift the self-loop free assumption by decomposing each self-loop into two (pointed) transitions in $TS(S)$ and recompose them once the global graph is constructed.
and unique outgoing edges, and if $\Lambda(v) \in \{\oplus, \emptyset\}$, $v$ has at least one incoming and one outgoing edge while $v$ has no outgoing edges if $\Lambda(v) = \emptyset$.

Label $a \rightarrow r : a$ represents an interaction where $a$ sends a message $a$ to $r$. A vertex with label $\emptyset$ represents the source of the global graph, $\emptyset$ represents the termination of a branch or of a thread, $\oplus$ indicates fork or joining threads, and $\oplus$ marks vertexes corresponding to branch or merge points, or to entry points of loops.

In step (4), a pre-global graph is obtained from the Petri net obtained after step (3) via a transformation which consists in, firstly, creating a vertex in the global graph for each place, transition, and element of the flow relation. Then these vertexes are connected via gates: a source vertex is connected to a vertex without predecessor, a sink vertex is connected to any vertex without successors, while transitions (resp. places) are connected to a $\oplus$-gate (resp. $\emptyset$-gate) if they have more than one predecessors or successors. Finally, each component of the graph is connected by merging "ports" corresponding to elements of the flow relation. The pre-global graph for $S_0$ (Figure 4) is given in Figure 5 (right).

A global graph is obtained from a pre-global graph by removing all unnecessary nodes (i.e., former places and transitions such as $p_0$ and $t_0$ in Figure 4) and relabelling events into interactions ($e$ is replaced by $1(e)$); e.g., the pre-global graph in Figure 5 becomes the global graph in Figure 2.

**Proposition 4.1.** Steps (2) to (4) are computable in polynomial time in the size of $N$.

We give the main result regarding the construction of a global graph from CFSMs. In Theorem 4.1 below, we formalise the relationship between the machines from which a global graph is constructed and its projections. Projecting a global graph $G$ can be done in two ways: (i) $G$ can be transformed into a Petri net whose reachability graph may be projected, similarly to the projection of $\text{TS}(S)$ (cf. Definition 3.3); or (ii) $G$ can be transformed into an automaton whose states are the nodes of $G$ and each transition is labelled by $(a \rightarrow r : a)_p$ if the source state corresponds to a vertex with label $a \rightarrow r : a$, and by $\epsilon$ otherwise. In order to recover local concurrency, we take the parallel composition of the automata resulting of the projection of each successor of a $\oplus$-gate. Finally, the resulting automaton is minimised with respect to language equivalence.

We write $G_{1G}$ for the projection of $G$ onto $p$, and give the formal definition in [5]. As an example, Figure 6 shows the minimised projection of $G_{1G}$ on $A$.

**Theorem 4.1** (Completeness). *Given a GMC system $S = (M_p)_{p \in P}$, let $G$ be the global graph built from $S$ and let $\text{TS}(S) = (N, n_0, E, \Rightarrow )$. If $\text{TS}(S)$ is self-loop free (i.e. $\forall n, n' \in N : n \Rightarrow n' \Rightarrow n \neq n'$), then $S$ is isomorphic to $(G_{1G})_{p \in P}$ the system made of the projection of $G$.*

The proof of Theorem 4.1 (cf. [3]) relies on the fact that each machine is preserved during the construction, i.e., (1) the projection of $\text{TS}(S)$ onto each $p$ is language equivalent with $M_p$, (2) the net obtained from $\text{TS}(S)$ via the algorithm in [19] is bisimilar to $\text{TS}(S)$, (3) each transformation preserves (weak) bisimilarity with

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**Figure 5.** Derived net (left), net after transformations (middle), and pre-global graph (right)

**Figure 6.** Projection of $G_{1G}$ onto $A$
the derived net, cf. Lemma 4.2 and (4) the transformation to a global graph is sound since the net is extended free choice.

5. Implementation and Experimental Evaluation

In order to assess the applicability of our work and to estimate the effectiveness of checking for the GMC condition as well as constructing a global graph, we have developed a prototype tool supporting our theory [6]. The tool (implemented in Haskell) takes as input a textual representation of a communicating system s, then builds TS(S) on which the representability condition and branching property are concurrently checked for (using HKC [13] to check for language equivalence). Then the tool constructs a global graph from TS(S) relying on Petri [2] (to derive a Petri net from TS(S)), and Graphviz [3] (to render global graphs).

Table 1 summarises the results of experiments conducted on a few real-world protocols mainly taken from the literature. For each protocol, the table reports the number of machines, the number of nodes and transitions in TS(S), whether it validates the GMC condition, the size of the constructed global graph, and the time it takes to check the condition and render its global graph (executions were on a 3.40GHz Intel i7 CPU with 16GB of RAM).

On most of the protocols the execution takes only a few seconds. To generate larger interesting examples, we tested systems consisting of the parallel composition of two protocols, e.g., Running Example × 2 is the parallel composition of two instances of the running example. Graphical representations of these protocols are in [6]. Observe that in general the size of the constructed global graph (i.e., the number of vertices) is significantly smaller than the size of TS(S), see Running Example × 2 for instance. We note that it is slightly more expensive to check the Running Example and the Logistic protocols. This is due to the fact that each of these protocols features at least one participant for which checking condition (2) of Definition 3.5 is not trivial, because they receive information about a choice from different participants, e.g., Carol in Sr.

On the other hand, checking the Alternating 3-bit protocol is more time consuming due to larger *-equivalence classes.

6. Related Work

Session Types In the context of multiparty session types, [28] first suggested a construction of a global protocol from a set of local session types, up to asynchronous sub-typing. A typing system which infers a global type [24] from a set of session types is given in [25]. Recursive constructions are restricted in this work, due to an inherently syntax-driven typing system, and multi-threaded participants are not supported (i.e., in terms of CFSMs, this means that mixed states are not allowed).

Example 6.1. Consider the GMC system of three machines below. Machine s chooses to either continue interacting with machine r (sending cont), or notify r that it wants to terminate (sending end), before collecting some information from machine p (collect).

This system is not accepted by the typing system in [25], because machine p is not involved in the recursion (cf. rules p| and [x] in [25]).

In [21], the authors study the synthesis of global types from basic CFSMs, that is deterministic, non-mixed (each state is either sending or receiving), and directed (for each state, its outgoing transitions are all labelled by an action sending to, or receiving from, the same participant). Basic CFSMs do not allow to model general concurrency at the local level, since a machine cannot have mixed states. Note that machines A, B, and C in Figure 1 are not directed. The present work covers a much larger set of global protocols than [21, 25, 28]: we support mixed and non-directed states (hence, multi-threaded participants are allowed), recursive protocols are no longer restricted by a syntax oriented formalism, and explicit fork/join control points may be constructed.

The first translation from generalised global types into CFSMs was given in [20], where only sound properties were presented. The generalised global types of [20] are strictly included in GMC systems (Definition 7.4). The complete characterisation of global graphs and a construction algorithm were left as open problems. This paper solves these problems.

Choreographies Other recent works [10,12,16,23] study the relationship between global and local specifications, but do not consider the problem of building global specifications from local ones. Namely, in [11], synchronisable systems are shown to preserve some reachability properties regardless the communication being asynchronous or synchronous. Essentially, systems are synchronisable if their synchronous behaviour is equivalent to their one-boundary asynchronous behaviour (considering send actions only). In [12], the authors tackle the problem of determining whether a choreography is realisable. Essentially, a choreography is realisable if “it is possible to build a distributed system that communicates exactly as the choreography specifies”. Choreographies in their work take the form of conversation protocols, that are finite state machines specifying the allowable sequence of interactions. A conversation protocol is akin to a global graph but without explicit construct for concurrent interactions, i.e., concurrent interactions must be specified by interleaving them.
We observe that both synchronisability and realisability conditions require strong properties on message ordering. In comparison, the GMC condition requires (i) the existence of a synchronous execution that encompasses all paths in each machine, and (ii) that each machine is either made aware every time a choice occurs or is not involved in the choice. In addition, a subtle difference between our machines and the machines in [11][12] is that each of the latter machines has a unique buffer from which it can receive messages. Namely, their model is not suitable to reason about a CS as the interleaving of several multiparty sessions (where each participant has different receiving buffers in each session). In particular, their model cannot be used to represent programs which communicate via point-to-point communications, such as TCP connections between pairs of participants. We discuss a few examples that illustrate the main differences between the two communication models.

Example 6.2. Consider the GMC system below.

In our model, machine $r$ receives messages from $a$ and $p$ from two different buffers; therefore, this system is safe (since machine $r$ is always able to read message $a$ and then consume $b$ regardless of the order in which the messages from $a$ and $p$ arrive). In a model where machine $r$ has only one FIFO buffer to receive both messages from $a$ and $p$, machine $r$ will end up in an unspecified reception configuration if message $b$ reaches the queue before $a$. The system above is not synchronisable, since its synchronous execution differs from its one-bounded asynchronous execution (considering send actions only). Symmetrically, its choreography is not realisable.

Observe that the system $\langle A, B, C_1, C_5 \rangle$, from Example 6.3, is unsafe in our communication model, but safe in theirs (where it is synchronisable). In that model, safety follows from the fact that machine $B$ would have only one buffer. Hence, if $A$ chooses the left-hand side branch, message $a$ will be in $B$’s queue before, thus $B$ must execute its left-hand side branch; while if $A$ chooses the right-hand side branch, $d$ will appear on $B$’s queue first and the latter will then execute its right-hand side branch. Finally, note that the GMC system (resp. choreography) in Example 6.1 is not synchronisable (resp. realisable) either due to the “race” between the send actions from machines $a$ and $p$.

Automata & MSC. The term synthesis of CFSMs has been used to describe the reduction of CS to a more manageable (and decidable) model, e.g., with partial order approaches (see [10] for a summary of recent results). The acceptation of the term synthesis in this context is to identify a system of CFSMs that realises a protocol described by an incomplete specification (such as in [9][29]). These approaches do not yield a global specification as instead achieved by our algorithm. In addition, our approach enables the verification of trace-based properties surveyed in [30]. For instance, the closed synthesis of CFSMs can be reduced to the construction from a regular language $L$ of a machine satisfying certain conditions related to buffer boundedness, deadlock-freedom, and words swapping.

In [27] a tool chain is given to synthesise an orchestrator (i.e., a message forwarder) from a set of finite-state machines communicating synchronously. This is transformed into a BPMN diagram via a Petri net transformation based on [19]. The work [22] gives an algorithm to compose several services. Each service is presented as an automaton and a set of automata are composed by a parallel product. The composite automaton is then transformed into a Petri net, using [19]. In both works, no result regarding safety or preservation of the behaviour of the original machines is given.

The work [1] studies whether Message Sequence Charts (MSC) imply unspecified scenarios (where MSCs are implemented by concurrent automata, but do not necessarily feature order-preserving communications). It gives conditions on MSCs for their implementation to be deadlock-free and realisable. MSCs are realisable if no other MSC may be inferable from them. It does not attempt to give an exhaustive global view of a distributed system, but focuses on identifying its possible misbehaviours.

7. Conclusions & Future Work
We have given a complete algorithm whereby one can build a global graph (choreography) from any generalised multiparty compatible (GMC) system. GMC systems form a new class of communicating systems, and we have proved that any system in this class is safe and there exist efficient algorithms to check GMC. Our work effectively uses the theory of regions [19], bridging a gap between a set of distributed uncontrolled behaviours (represented by CFSMs) and well-structured graphical session types, while offering a scalable implementation for our framework.

Since the original machines can be recovered by projecting the constructed global graph (by Theorem 5.1), we can use our framework to develop a software development life cycle based on choreographies: a specification written as a choreography is projected onto a set of local models which will then be refined against their implementations. Such an approach can also be used to reverse-engineer existing distributed systems. We are currently collaborating with the Zero Deviation Lifecycle project [4] which proposes a platform to attain “near-zero defect leakage across the various phases of the software development lifecycle”. Updating global scenarios against local models plays an important role in different stages of software life cycle in this architecture. Our framework applies naturally to this platform, which notably uses BPMN 2.0 Choreography specifications and tools.

We also plan to investigate a relaxed version of the GMC condition which would allow to build global graphs whose projections are equivalent to the original system, up to asynchronous order-preserving communication [28].

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References
A. Equivalences between Petri Nets

We give the formal definitions of the reachability graph of a Petri net and weak-bisimulation, which are used in Section 4.

Definition A.1 (Reachability graph [19]). Given \( N = (P, T, F, m_0) \), we say that a transition \( t \in T \) is enabled at marking \( m_1 \) if all its input places are marked. An enabled transition \( t \) may fire, producing a new marking \( m_2 \) with one less token in each input place and one more token in each output place. We write \( m_1 \xrightarrow{t} m_2 \), if \( m_2 \) is reachable from \( m_1 \) by firing \( t \), and write \( \xrightarrow{} \) for the reflexive transitive closure of \( \xrightarrow{} \).

The reachability graph of \( N \) is the transition system \( RG(N) = (M, m_0, E, \rightarrow) \) such that \( M = \{ m \mid m_0 \rightarrow m \} \);

\[ \rightarrow = \{(m_1, lab(t), m_2) \mid m_1, m_2 \in M \land m_1 \xrightarrow{t} m_2 \} \text{ (where } lab(t) = e \text{ if the label of } t \text{ is } e, \text{ and return the label } e \text{ of } t \text{ otherwise); and} \]

\[ E = \{ e \mid \exists (m_1, e, m_2) \rightarrow \land e \neq e \}. \]

\[ \text{Let } m \xrightarrow{e} m' \iff (m, e, m') \rightarrow \text{ and } m \Rightarrow m' \iff m(\xrightarrow{e} \rightarrow) \Rightarrow (\xrightarrow{e} \rightarrow) m', \text{ with } e \neq e. \]

The notion of weak bisimulation between two transition systems is given in Definition 4 (adapted from [25]).

Definition A.2 (Weak bisimulation). Let \( T = (M, m_0, E, \rightarrow) \) be a transition system. A weak bisimulation on \( T \) is an equivalence relation \( \equiv \subseteq M \times M \) s.t. for all \((m_1, m_2) \in \equiv\), the following holds

\[ m_1 \xrightarrow{e} m_1' \text{ implies that there is } m_2 \text{ such that } m_2 \Rightarrow m_1' \text{ and } (m_1', m_2') \in \equiv; \]

\[ m_2 \Rightarrow m_2' \text{ implies that there is } m_1' \text{ such that } m_1' \Rightarrow m_2' \text{ and } (m_1', m_2') \in \equiv. \]

Two states \( m_1 \) and \( m_2 \) are called weakly bisimilar on \( T \), written \( m_1 \equiv m_2 \), iff \((m_1, m_2) \equiv \equiv \) for some weak bisimulation \( \equiv \). Two transition systems \( T_i = (M_i, m_{i0}, \hat{E}_i, \rightarrow_i) \), \( i \in \{1, 2\} \), such that \( M_1 \cap M_2 = \emptyset \), are weakly bisimilar, written \( T_1 \approx T_2 \), if given

\[ \hat{M}' = M_1 \cup M_2 \cup \{m_0 \} \text{ and } \hat{E}' = \hat{E}_1 \cup \hat{E}_2 \],

\[ T' = \{ (m_0, m_0, \hat{E}', \rightarrow_1 \cup \rightarrow_2 \cup \{m_0, e, m_0'\} : (m_0, e, m_0') \in \hat{M}' \} \] holds.

B. From Petri Nets to Global Graphs

In this section we give the detailed transformations omitted in Section 4. The algorithm to construct a global graph \( G \) from a synchronous transition system \( TS(S) \) consists of the following steps: (1) using the algorithm of Cortadella et al. [19], we derive a Petri net \( N \) from \( TS(S) \); (2) we transform \( N \) so that its initial marking consists of exactly one place (Transformation B.3 below); (3) we join transitions whenever possible, so to make joins and forks explicit (Transformation B.5 below); (4) we transform the net of (3) into a pre-global graph (Transformation B.3 below); finally, we “clean-up” the pre-global graph of any unnecessary vertexes so to obtain a global graph (Transformation B.5 below).

Definition B.1 (Labelled net). A labelled Petri net, or net, \( N \) is a quadruple \( (P, T, F, m_0) \), with a set of places (ranged over by) \( P \), \( T \) a set of transitions (ranged over by) \( T \), \( F \subseteq (P \times T) \cup (T \times P) \) the flow relation, and \( m_0 \) the initial marking. Each transition \( t \in T \) is labelled with an event \( e \in \hat{E} \), or marker \( e \) (the latter representing a silent transition). We let \( x \) range over elements of \( P \cup T \). As usual, \( \ast \) (resp. \( \ast \)) is the preset (resp. postset) of \( x \). A net is called safe if, for all reachable markings, no more than one token can appear in each place; in which case the reachable markings (including \( m_0 \)) are sets of places. A net is extended free-choice if \( \forall p \in P, \forall t \in T : (p, t) \in F \implies ((p \times \ast) \cup (\ast \times p)) \subseteq F. \)
In the second step (2), we transform a Petri net obtained from Cortadella’s algorithm into a Petri net whose initial marking consists of exactly one place. This allows us to construct a global graph that has a unique starting point (source).

**Transformation B.1** (One-source net). Given a labelled Petri net \( N = (P, T, F, m_0) \), the one-source net of \( N \) is \( \overline{N} = (P \cup \{p_0\}, \overline{T} \cup \{t'\}, \overline{F}, \{p_0\}) \) such that \( p_0 \not\in P \) and \( t' \not\in T \) is labelled by \( \epsilon \), and \( \overline{F} = F \cup \{\{p_0, t'\}\} \cup \bigcup_{p \in m_0 \setminus \{p_0\}} \{\{t', p\}\} \).

**Proposition B.1.** Transformation [B.1] is computable in linear time in the size of \( m_0 \).

We can now state the following result, formalising the soundness of Transformation [B.1].

**Lemma B.1.** If \( T \) is the reachability graph of the Petri net \( N \) obtained from \( TS(S) \) via the algorithm in Table 29, and \( T' \) is the reachability graph of the Petri net obtained after applying Transformation [B.1], then \( T \approx T' \).

Next, Transformation [B.2] ensures that parallel gates are used “as much as possible” in the graph (instead of mixing choice and parallel gates). In fact, Transformation [B.2] joins sets of places having the same preset or postset to decrease the number of choice gates.

**Transformation B.2** (Joined net). The joined net of \( N = (P, T, F, m_0) \) is a net \( \overline{N} = (P', T', F', m_0) \) such that the following transformations are applied repeatedly:

1. for all maximal \( X \subseteq P \) s.t. \( |X| > 1 \) and \( \forall p_1, p_2 \in X : *p_1 = \overline{p}_1 \wedge |p_1| > 1, P' = P \cup \{p'\} \) and \( T' = T \cup \{t'\} \) with \( p \not\in P \) and \( t' \not\in T \) and labelled by \( \epsilon \); also, chosen \( p \in X, F' = \{(p', t')\} \cup (p' \times \{t'\}) \cup (t' \times X) \cup F \bigcup_{x \in X} (x \times X) \).

2. for all maximal \( X \subseteq P \) s.t. \( |X| > 1 \) and \( \forall p_1, p_2 \in X : p_1 = p_2 \wedge |p_1| > 1, p' = P \cup \{p'\} \) and \( T' = T \cup \{t'\} \) with \( p \not\in P \) and \( t' \not\in T \) and labelled by \( \epsilon \); also, chosen \( p \in X, F' = \{\{(p', t')\} \cup (p' \times \{t'\}) \cup (X \times \{t'\}) \} \cup F \bigcup_{x \in X} (x \times X) \).

Note that the definition of \( F' \) does not depend on the choice of \( p \).

**Proposition B.2.** Transformation [B.2] is computable in polynomial time in the size of \( N \).

Since we are working with safe nets, we have the result below.

**Lemma B.2.** If \( T \) (resp. \( T' \)) is the reachability graph of the Petri net \( N \) obtained after Transformation [B.1] (resp. Transformation [B.2]) then \( T \approx T' \).

**Definition B.2** (Graph composition). Let \( \mathcal{N}_i = (P_i, T_i, F_i, m_{0i}) \) with \( i \in \{1, 2\} \) be two nets and let \( \mathcal{G}_i = \langle V_i, A_i, A_r \rangle \) be two graphs such that \( \mathcal{V}_i = P_i \cup T_i \cup F_i, i \in \{1, 2\} \), the composition of \( G_1 \) and \( G_2 \), denoted by \( G_1 \circ G_2 \) is a graph \( \langle \mathcal{V}, \mathcal{A}, \mathcal{A}_r \rangle \) defined as:

\[
\begin{align*}
\mathcal{V} &= \{v \in V_1 \mid v \in F_1 \} \cup \{v \in V_2 \mid v \not\in F_2 \} \\
\mathcal{A} &= \{(A_1 \cup A_2) \cap \mathcal{V} \times \mathcal{V} \cup \{(v, v') \mid \exists v'' \in F_1 : (v, v'') \in A_1, (v'', v') \in A_2 \} \}
\end{align*}
\]

Intuitively, the composition of the graphs consists of (1) the union of the two sets of vertices, except flow elements \((p, t)\) and \((t, p)\) if they appear in both \( F_1 \) and \( F_2 \); and (2) the union of the two sets of arcs between vertices in \( V \), and each pair of arcs of the form \((v, (x, x'))\) or \([(x, x'), \gamma]\) is replaced by a single arc \((v, v')\).

**Transformation B.3** (Pre-global graph). The pre-global graph of \( N = (P, T, F, \{p_0\}) \) is a tuple \( \langle \mathcal{V}, \mathcal{A}, \mathcal{A}_r \rangle \) such that \( V = P \cup T \cup F \); \( \mathcal{A} \) is a labelling function such that \( \mathcal{A}(v) = v \) if \( v \in P \cup F \) or \( v \in T \) labelled by \( \epsilon \), and \( \mathcal{A}(v) = E \cup \{\#, \emptyset, \ldots, \# \} \) otherwise; and \( A \) is given by:

\[
\begin{align*}
T_{\mathcal{G}}(N) &= \bigcup_{x \in P \cup T} T_x(x) \cup T_{\mathcal{G}}(x) \\
T_x(x) &= \left\{ \begin{array}{ll}
(x', x) & \text{if } x = \emptyset \\
(x', x) & \text{if } x = \{x'\} \\
(x', x) & \text{if } x = \{x_1, \ldots, x_k\} \\
(x, x) & \text{if } x = \emptyset \\
(x, x) & \text{if } x = \{x'\} \\
(x, x) & \text{if } x = \{x_1, \ldots, x_k\}
\end{array} \right.
\end{align*}
\]

with \( k > 0 \), \( \emptyset = \{x \in P \} \) if \( x \in P \), and \( \emptyset = \{x \} \) if \( x \in T \).

The pre-global graph of \( S_{\mathcal{G}} \) (Figure 1) is given in Figure 5 (right). Observe that all the vertexes of the form \((x, x')\), corresponding to an element of the flow relation, are removed as part of the graph composition (Definition B.2).

**Proposition B.3.** Transformation [B.3] is computable in polynomial time in the size of \( N \).

We define the final transformation which cleans up a pre-global graph by removing unnecessary vertexes and arcs.

**Transformation B.4.** A global graph \( G = \langle \mathcal{V}, \mathcal{A}, \mathcal{A}_r \rangle \) is obtained from a pre-global graph \( \langle P \cup T \cup F, \mathcal{A}, \mathcal{A}_r \rangle \) by applying the following transformation: (1) replace each pair of transition \((x, p), (p, x') \in A'\) by \((x, x') \in A\); (2) replace each pair of transition \((x, t), (t, x') \in A'\), with \( t \) labelled by \( \epsilon \), by \((x, x') \in A\); and (3) label each \( r \) which is labelled by \( (q_2, q_1, a \rightarrow r : a) \) in \( N \), by \( a \rightarrow r : a \).

**Proposition B.4.** Transformation [B.4] is computable in polynomial time in the size of \( N \).
C. Projections of Global Graphs

The definition of the projection of a global graph onto a participant, used in Section 3, is given below. We first define a parallel composition of automata, which is required to project global graphs with a participant appearing in different threads. We define the *a function on vector of states:

\[ q^a \overset{\text{def}}{=} \begin{cases} q_i^* & \text{if } q = (q_1, q_2) \text{ and } q_i^* = q_i^* \\ \hat{q} & \text{otherwise} \end{cases} \]

We overload it on sets of vector of states, i.e., \( Q^a \overset{\text{def}}{=} \{ q^a \mid q \in Q \} \). We define:

\[ q \subseteq q' \iff \begin{cases} q = q' \text{ and } q^a = (q^a) \\ q = (q_1, q_2) \wedge q \subseteq q_i \wedge i \in \{1,2\} \end{cases} \]

We write \( q \not\subseteq q' \) iff \( \neg(q \subseteq q') \) and we overload the operator \( \subseteq \) on set of states such that \( q \subseteq Q \iff \exists q' \in Q : q \subseteq q' \).

**Definition C.1** (Parallel composition). The composition of \( M_i = (Q_i, q_i^0, \delta_i, \iota \in \{1,2\}, \) written \( M_1 \parallel M_2, \) is the automaton \((Q_1 \times Q_2)^a, (q_1^0, q_2^0)^a, \delta_i \) s.t.

\[ ((q_1, q_2)^a, \ell, (q_1', q_2')^a)^a \in \delta \]

\[ \iff \begin{cases} (q_1, q_2)^a, (q_1', q_2')^a \in \delta_i \\ q_1 = q'_1 \in Q_i \text{ and } 1 \neq j \in \{1,2\} \end{cases} \]

Notice that \( || \) is a commutative and associative operation. Below, we give the definition of the projection function.

**Definition C.2** (Projection). Given \( G = \langle V', A, \Lambda \rangle \) and \( v \in V' \), the projection of \((G,v,V)\) onto \( p \), denoted by \((G,v,V)\) \( |_p \), is defined as follows:

\[ (G,v,V) |_p = \begin{cases} \langle \{v\}, v, \emptyset \rangle & \text{if } A(v) = \emptyset \text{ or } v \in V \\ \langle Q \cup \{v\}, v, \emptyset \cup \{(v, v')\} \rangle & \text{if } v \in v^*, \ell = s \rightarrow t : a \mid_p \\ \text{ and } (Q', \ell, v') = (G', V' \cup \{v\}) \rangle \end{cases} \]

\[ \langle \{v\} \cup \bigcup_{v' \in v^*} \{Q,v, \emptyset \cup \{(v, v')\}\} \rangle \text{ if } A(v) \neq \emptyset \text{ and } (Q', v, \emptyset) = (G', V' \cup \{v\}) \rangle \]

Given a vertex \( v \in V' \) such that \( A(v) = Q \), the projection of \( G \) onto \( p \), written \( G |_p \), is the automaton, minimised wrt. language equivalence, \( (Q, q_0, \delta, \hat{A}) \) with \((G,v,V) |_p = \langle Q, q_0, \delta, \hat{A} \rangle \).

The projection of a global graph onto \( p \) uses the auxiliary function \((G,v,V) |_p \). The function takes the following parameter, \( p \): the identifier of the participant onto the projection is invoked, \( G \): the global graph to be projected, \( v \): a node in \( G \) used as an initial node for the projection, and \( V' \): a set of visited nodes.

If \( v \) has already been visited or it is a sink node, then a single state automaton is returned. If \( v \) is labelled by \( s \rightarrow v : a \), then the projection of it successor is connected to \( v \) by a transition labelled by either \( px^a \), \( px^a \), or \( \emptyset \). If \( v \) is a choice gate or the source node, then the projection of each successor of \( v \) is connected to \( v \) by an \( e \) transition from \( v \), as returned. If \( v \) is a parallel gate, then the parallel composition of the projections of its successors are returned, connected to \( v \) by an \( e \) transition. The parallel composition of automata uses Definition C.1 so that state identities are normalised and visited nodes are comparable with nodes produced by composing automata.

D. Proofs of Section 3

In this section, we first prove the key properties of the relations defined in the paper. Then we prove the main theorem (soundness), the reachability lemma and complexity results stated in Section 3.

We use the following functions in the proofs:

1. \( \text{snd}((q_s. q_r, s \rightarrow r : a)) = \text{snd}(s \rightarrow r : a) \)
2. \( \text{rcv}((q_s. q_r, s \rightarrow r : a)) = \text{rcv}(s \rightarrow r : a) \)
3. \( \text{id}((q_s. q_r, s \rightarrow r : a)) = \text{id}(s \rightarrow r : a) \)

**D.1 Properties of \( TS(S) \)**

**Lemma D.1.** Let \( S = (M_p)_{p \in P} \) be a GMC system and \( TS(S) = (N, n_0, \bar{E}, \Rightarrow) \), for all \( p \in P \) and for all \( q \in Q_p \) such that \( (q, q_1. q_2) \in \delta_p \) for some \( q_1, q_2 \in Q_p \), there is \( n_1, n_2 \in N \) such that \( n[p] = q \).

\[ \cdot n \overset{\delta_1}{\Rightarrow} n_1 \text{ with } n_1 \overset{p}{\Rightarrow} e, e_1 \overset{p}{\Rightarrow} e_1; \text{ and} \]

\[ \cdot n \overset{\delta_2}{\Rightarrow} n_2 \text{ with } n_2 \overset{p}{\Rightarrow} e, e_2 \overset{p}{\Rightarrow} e_2. \]

**Proof.** This result follows directly from the representability condition.

**Lemma D.2.** Let \( S = (M_p)_{p \in P} \) be a system and \( TS(S) = (N, n_0, \bar{E}, \Rightarrow) \). For all \( n \neq n' \in N \), if \( n \overset{e}{\Rightarrow} n' \), then \( n' \not\Rightarrow e. \)

**Proof.** By contradiction, assume there are \( n \neq n' \in N \) such that \( n \Rightarrow n' \) and \( n' \Rightarrow n'' \), where \( e = (q_s.q_r, s \rightarrow r : a) \). There are two cases depending on whether the events come from different events in \( \delta \).

1. Assume \((n, e.n', (n', e', n'') \in \delta \), then it must be the case that \( (q_s, sx^a.a, q_{a. a}) \in \delta_a \) and \( (q_r, sx^a.a, q_{a. a}) \in \delta_r \), i.e., there is a corresponding self-loop in machines \( s \) and \( r \). Since the local source state and target states of both machines are the same, this contradicts the assumption that \( n \neq n' \).

2. Assume \((n, e.n', (n', e', n'') \in \delta \), with \( e \neq e' \) or \( e' = (q_s', q_r', s \rightarrow r : a) \), and \( e \not\Rightarrow e' \). We must have that \( \delta_a(q_s, sx^a.a) = q_{a. a} \) and \( \delta_r(q_r, sx^a.a) = q_{a. a} \); since \( e \not\Rightarrow e' \) we must have that \( q_a \neq q_{a. a} \) or \( q_a \neq q_{a. a} \). Take \( q_r \neq q_{a. a} \). We must have \( (q_s, sx^a.a, q_{a. a}'), \)

\( (q_r, sx^a.a, q_{a. a}') \in \delta_r \). This implies that we have

\[ (q_s, sx^a.a, q_{a. a}'), (q_s, sx^a.a, q_{a. a}'') \in \delta_r. \]

Since \( \delta_r(a) \in (q_r, q_{a. a}') \),

the cases where \( q_s \neq q_1 \) and \( q_a \neq q_{a. a} \) are similar. Hence, by Definition 3.3, we conclude that \( \neg(e \not\Rightarrow e') \) which contradicts our hypothesis.

**D.2 Properties of \( \prec \)**

In this section, we fix \( S = (M_p)_{p \in P} \) and \( TS(S) = (N, n_0, \bar{E}, \Rightarrow) \).

**Lemma D.3.** Let \( n, n', n'' \in TS(S) \), if \( n \prec n' \) and \( n' \prec n'' \), then \( n \prec n''. \)

**Proof.** Assume \( n \prec n' \) and \( n' \prec n'' \), by definition of \( \prec \), we have

\[ n_0 \Rightarrow n \Rightarrow n' \Rightarrow n'' \]

which implies that there is a simple path from \( n \) to \( n'' \).

By contradiction, assume \( \neg(n \prec n'') \). This implies that either (i) \( n \Rightarrow n'' \), which contradicts the fact that \( n' \prec n'' \); or (ii) there is a simple path from \( n_0 \) to \( n \) that includes \( n'' \), thus there is also a simple path from \( n_0 \) to \( n'' \) that includes \( n'' \), which contradicts our assumption.
Lemma D.4. Let \( n_1 \ldots n_k \) be a simple path in \( TS(S) \), such that \( n_k \Rightarrow n_1 \). For all \( 1 \leq i, j \leq k \), if \( n_i \not< n_j \), then \( \neg(n_i < n_j) \).

Proof. Take \( n_i \) and \( n_j \) such that \( n_i \not< n_j \). This means that \( n_j \) never appears on a path from \( n_0 \) to \( n_i \). Since \( n_1 \ldots n_k \) forms a cycle, we must have \( n_i \Rightarrow n_j \) and \( n_j \Rightarrow n_i \). Thus we have

\[
0 \Rightarrow * \quad n_i \Rightarrow * \quad n_j \Rightarrow * \quad n_i
\]

and there is a simple path from \( n_0 \) to \( n_j \) that includes \( n_i \) (cf. simple path assumption), which implies that we cannot have \( n_j \not< n_i \).

D.3 Properties of \( \ln(n,e_1,e_2) \)

Lemma D.5. Let \( S = (M_p)_{\text{pet}} \) and \( TS(S) = (N, n_0, \hat{E}, \Rightarrow) \). For all \( n \in N \), if \( n \Rightarrow n \), \( n \Rightarrow \hat{n} \), and \( e_1 \neq e_2 \), then \( \ln(n,e_1,e_2) \neq \emptyset \).

Proof. By assumption, there is at least one node that fires both \( e_1 \) and \( e_2 \) and is reachable from \( n \), i.e., \( n \).

If \( n \) is the only node from which \( e_1 \) and \( e_2 \) are fireable, \( n \Rightarrow n_1 \) and \( n \Rightarrow n_2 \), then \( \ln(n,e_1,e_2) = \{(n_1,n_2)\} \). Note that the result also holds if \( n \Rightarrow n \) since we have \( \neg(n \not< n) \), by definition of \( \not< \). If \( n \) is not the only node from which \( e_1 \) and \( e_2 \) are fireable, then there are two cases:

1. None of these nodes is reachable from \( n \), in which case the result is the same as above, i.e., \( \ln(n,e_1,e_2) = \{(n_1,n_2)\} \).

2. Let \( N_0 \equiv \{n'| n \Rightarrow n' \land n' \not< n' \land n' \not< n'' \} \), note that \( n \in N_0 \) and, by contradiction, assume \( \ln(n,e_1,e_2) = \emptyset \). Hence, none of the nodes in \( N_0 \) satisfy the condition, i.e.,

\[
\forall n' \in N_0: \exists n'' \in N_0: \quad n' \Rightarrow n'' \land n'' < n
\]

Since the number of nodes in \( TS(S) \) is finite, we must have a cycle in \( N_0 \).

By Lemmas D.3 and D.4 we know that \( \Rightarrow \) forms a strict partial order on the nodes of each cycle in \( N_0 \). Thus, for each simple cycle consisting of nodes in \( N_0 \), each top element \( n' \in N_0 \) we have:

\[
\forall n'' \in N_0': \forall n' \Rightarrow n'' \Rightarrow (n'' < n'')
\]

Let \( N''_0 \subseteq N_0 \) be the set of all top elements of each simple cycle in \( N_0 \), none of the nodes in \( N''_0 \) satisfy the condition only if

\[
\forall n' \in N_0': \forall n'' \in N''_0: \quad n' \Rightarrow n'' \land n'' < n'
\]

We show that given \( n_i, n_j \in N''_0 \) such that \( n_i \Rightarrow n_j \) and \( n_j \not< n_i \), we cannot have \( n_j < n_i \), i.e., there is no \( \not< \)-cycle between the nodes.

(a) If \( \neg(n_i \Rightarrow n_j) \), we cannot have \( n_j < n_i \) (and there is no cycle between the two nodes).

(b) If there is a simple cycle between the two nodes, then either the nodes are \( \not< \)-incomparable (i.e., we have a contradiction) or \( n_i < n_j \land \neg(n_j < n_i) \), cf. Lemma D.4.

(c) If there is (only) a non-simple cycle such that \( n_i \Rightarrow n_j \), \( n_j \Rightarrow n_i \), then, each path between \( n_i \) and \( n_j \) must go through \( n_3 \) such that \( n_3 \not< n_i \) and \( n_3 \not< n_j \), which contradicts \( n' \Rightarrow n'' \).

Given that there cannot be a \( \not< \)-cycle amongst the nodes in \( N''_0 \), the function must return at least one element (i.e., at least one top element satisfies the condition).

Lemma D.6. Let \( S = (M_p)_{\text{pet}} \) be a GMC system and \( TS(S) = (N, n_0, \hat{E}, \Rightarrow) \). For all \( n \in N \), if \( n \Rightarrow n_{1} \) and \( n \Rightarrow n_{2} \), then either

1. \( n_{1} \Rightarrow n' \) and \( n_{2} \not< n' \), for some \( n' \in N \), or
2. \( \text{snd}(e_1) = \text{snd}(e_2) \)

Proof. Direct from Lemma D.3 and Definition 3.5.

Lemma D.7. Let \( S = (M_p)_{\text{pet}} \) be a GMC system and \( TS(S) = (N, n_0, \hat{E}, \Rightarrow) \). For all \( n \not< n' \in N \), if \( n \not< n_1 \), \( n' \not< n_2 \), and \( n \not< n' \) then \( n_1 \not< n_2 \).

Proof. There are four cases depending on the machines involved in \( e \) and \( e' \), and whether the event fired from \( n \) is the same as the event fired from \( n' \) in \( \hat{\delta} \).

1. If \( \text{id}(e) \cap \text{id}(e') = \emptyset \), i.e., the events are independent, the result follows by definition of \( TS(S) \); we have \( n_1 \not< n_2 \).

2. If \( \text{id}(e) = \text{id}(e') \) and \( (n,e,n_1) \not< (n',e',n_2) \), then we have \( \forall p \in \text{id}(e): \quad n[p] = n'[p] \), which contradicts the fact that \( n \not< n' \).

3. If \( \text{id}(e) \cap \text{id}(e') \neq \emptyset \) and \( (n,e,n_1) \not< (n',e',n_2) \), then we must have, without loss of generality, \( (n,e,n_1), (n',e',n_2) \in \hat{\delta} \), such that \( e \neq e' \) and \( e \neq e'' \). By definition of \( \not< \), we must have \( e = (q_a,q_{a'},s \rightarrow \tau,a) \) and \( e' = (q_a,q_{a''},s \rightarrow \tau,a') \), with \( q_a \neq q_{a'} \) or \( q_{a'} \neq q_{a''} \). For each \( p \in \text{id}(e) \cap \text{id}(e') \), we have \( (n[p],e'[p],n'[p]) \in \delta_p \) by assumption and, by definition of \( \not< \), we must also have \( (n_1[p],e'[p],n_1'[p]) \in \delta_p \). Thus, if \( \text{id}(e) = \text{id}(e') \), we have the result immediately. If \( \text{id}(e) \neq \text{id}(e') \), then the machine not involved in \( e \) is still able to interact with \( p \) so that \( e' \) and we have the result.

4. If \( \text{id}(e) \cap \text{id}(e') \neq \emptyset \), \( \text{id}(e) \neq \text{id}(e') \), and \( (n,e,n_1), (n',e',n_2) \in \hat{\delta} \), then it must be the case that one of the machine has a self-loop, so that we have three machines of the form:

\[
\begin{array}{c}
\hat{r} \quad q_a \\
\hat{s} \quad q_a \\
\end{array}
\]

\[
\begin{array}{c}
\hat{r} \quad q_{a'} \\
\hat{s} \quad q_{a'} \\
\end{array}
\]

\[
\begin{array}{c}
\hat{r} \quad q_{a''} \\
\hat{s} \quad q_{a''} \\
\end{array}
\]

If there is \( (q_a,q_{a'},q_{a''}) \in \delta_p \), then we have the result immediately (as above), otherwise there are two cases, depending on whether \( (n_1,n') \in \ln(n,e,e') \).

(a) If \( (n_1,n') \in \ln(n,e,e') \), then either the two branches commute, and we have the result; or case 2 of Definition 3.5 must hold. However, in both branches, machine \( \tau \) is able to execute \( e'_{\tau} \) as a first action (since \( \tau \neq \text{id}(e') \) by assumption). This means that the two branches must merge, i.e., once in state \( q_{a'_{\tau}} \), \( \tau \) must be able to interact with \( \tau' \) such that \( \tau' \) reaches \( q_{a''_{\tau}} \). In addition, by representability, the self-loop at \( \tau \) must appear in \( TS(S) \), which means there must be two branches in \( TS(S) \), one that leads to a configuration where machines \( \tau, \tau' \) and \( \tau'' \) are in states \( q_{a_{\tau}}, q_{a'_{\tau}} \) and \( q_{a''_{\tau}} \), respectively (where the self-loop is not represented); and one branch that leads to a configuration where the self-loop appears. This contradicts case 2 of Definition 3.5 since \( \tau \) would have the same first actions in both branches, while its behaviour must be different in both branches (i.e., the self-loop appears in one but not in the other).
(b) If \((n_1, n') \notin \ln(n, e, e')\), we have the following situation (by definition of the last node function):

\[
\begin{array}{c}
\alpha_1 \\
\vdots \\
\alpha_2 \\
\hat{n} \\
\hat{n}_1 \\
\hat{n}_2 \\
\hat{n}'_2 \\
\end{array}
\]

Indeed, since \((n_1, n') \notin \ln(n, e, e')\), there must be a successor of \(n\) that fires both \(e\) and \(e'\), i.e., \(\hat{n}\) in the diagram above. Note that by Lemma D.2 \(\hat{n}\) cannot be a target of \(e\) or \(e'\).

i. If \((\hat{n}_1, \hat{n}_2) \in \ln(\hat{n}, e, e')\). We show that \(e\) must also be fireable from \(\hat{n}_2\), by contradiction. Since \(e'\) is a self-loop on \(\hat{n}\) and the machines are deterministic, \(\hat{n}\) is still able to fire \(e'\) after \(e'\) has fired, and since \(x \notin \hat{d}(e')\), the only way \(e\) would not be fireable from \(\hat{n}_2\) is if \(x \notin \hat{d}(e')\) and there is \(q \in Q_e : \hat{n}[x], e', e \notin e\), which contradicts the fact that \(\hat{n} \Rightarrow \hat{n}_2\). We can now repeat the argument with \(\alpha_1 = e', \hat{n} = n'\), and \(\hat{n}_1 = \hat{n}_2\).

ii. If \((\hat{n}_1, \hat{n}_2) \notin \ln(\hat{n}, e, e')\), then we can repeat the argument (cf. faded part of the picture) until we reach a pair of nodes that is in \(\ln(e, e', e')\). We know that such a pair exists by Lemma D.5.

Definition D.1. Let \(S = (M_p)_{p \in \mathbb{P}}\) be a GCM system and \(TS(S) = (N, n_0, E, \Rightarrow, \rho)\). For all \(p \in \mathbb{P}\) and all \(q \in Q_p\), if \((q, \ell_1, q_1), (q, \ell_2, q_2) \in \delta_p\) with \(\ell_1 \neq \ell_2\), we write \((q, \ell_1, q_1) \sim (q, \ell_2, q_2)\) iff there exists \(n \in N\) such that:

1. \(n[p] = q\).
2. \(n \Rightarrow n_1, n \Rightarrow n_2, \quad n \Rightarrow n_1 \Rightarrow n_2, \quad n \Rightarrow n_1 \Rightarrow n_2,\) and \(n \Rightarrow n_1 \Rightarrow n_2\).

3. \(\{q, q_1, q_2\} \in \delta_p, \exists q, q_2, q_2 \in \delta_p : (q, \ell_1, q_1) \sim (q, \ell_2, q_2)\) (3)

By Lemma D.1 for each \((q, \ell_2, q_2) \in \delta_p\) there is \(n\) such that \(n[p] = q\).

\[
n \Rightarrow n_1 \Rightarrow n_2 \Rightarrow n_3 \Rightarrow n_2 \Rightarrow n_1 \Rightarrow n_2, \quad n \Rightarrow n_1 \Rightarrow n_2, \quad \hat{n}_1 \Rightarrow \hat{n}_2, \quad \hat{n}_1 \Rightarrow \hat{n}_2, \quad \hat{n}_1 \Rightarrow \hat{n}_2, \quad \hat{n}_1 \Rightarrow \hat{n}_2.
\]

Choose \(n, \ell_2, \pi_1, \pi_2\) such that \(\pi_2\) is the smallest, i.e., there is no other node \(n'\) such that \(\pi_2\) holds and \(n'\) is strictly smaller, for some \(\ell_2\). In other words, \(n\) is the last node from which \(p\) can “choose” to fire either \(\ell_1\) or another action \((\ell_2)\). Let \(k = \text{hd}(LT(S,n,p))\) and recall that \(\ell_1, \ell_2 \in \text{hd}(LT(S,n,p))\) by assumption.

We first show the result for \(k = 2\).

1. If \(n\) is such that \(n \Rightarrow n_1, n \Rightarrow n_2, \) with \(e_1[p] = \ell_1, (i \in \{1, 2\})\), then, by Lemma D.5 there is \(\hat{n}, \hat{n}_1, \hat{n}_2 \in N\) such that \(\hat{n} \Rightarrow \hat{n}_1, \hat{n} \Rightarrow \hat{n}_2, \) and \((\hat{n}_1, \hat{n}_2) \subseteq \ln(\hat{n}, e_1, e_2)\). Thus, we have \((q, \ell_1, q_1) \sim (q, \ell_2, q_2)\).

2. If \(n\) is such that \(n \Rightarrow n_1, n \Rightarrow n_2, \) with \(e_1[p] = \ell_1, (i \in \{1, 2\})\), and \((n_1, n_2) \notin \ln(n, e_1, e_2)\), then we have \((q, \ell_1, q_1) \sim (q, \ell_2, q_2)\), since \(n\) is the latest node and \(k = 2\).

3. If \(n\) is such that \(n \Rightarrow n_1, n \Rightarrow n_2, \) with \(e_1[p] = \ell_1, (i \in \{1, 2\})\), and \((n_1, n_2) \notin \ln(n, e_1, e_2)\), we must have the following situation, where \(e'_1[p] = \ell_1\).

Since \((n_1, n_2) \notin \ln(n, e_1, e_2)\), it must be the case that there is a successor of \(n\) that is able to fire both \(e_1\) and \(e_2\), i.e., \(\hat{n}\) here. Note that by Lemma D.2 \(\hat{n}\) cannot be a target of \(e_1\) or \(e_2\). In addition, by Lemma D.7 there must be an even path between \(n_1\) and \(n_1\) (cf. faded \(\pi\)-labelled edges in the diagram above).

By assumption \((k = 2)\), we have \((\ell_1) = \text{hd}(LT(S,n,p))\) (otherwise, \(n\) would not be the latest node); thus \(\ell_2 \neq \pi_1[p] = \ell_1\).

For \(i \in \{1, 2\}\), we reason as follows:

- If case (1) of Definition D.5 applies to the branching at \(n_i\), then \(e'_i\) can also be fired from \(n_i\), and we have the result.

- If case (2) of Definition D.5 applies to the branching at \(n_i\), then either (i) machine \(p\) must execute an action different from \(\ell_1\) in the branch corresponding to \(\pi_i\). However, this contradicts the fact that \(\ell_2\) cannot appear in \(\pi_i[p]\). Or, (ii) there is a merging node \(n'\), reachable from both \(n_1\) and \(n_2\), and two paths (from \(n'\) to \(n_i\)) such that machine \(p\) has an empty behaviour on both; which contradicts our assumption that \(n\) is the last node such that \(\ell_1\) and \(\ell_2\) are fireable.

Since \(\ell_1\) and \(\ell_2\) are the only actions fireable from \(q\), we conclude that each action is executed from \(n_1\) and \(n_2\), respectively, and we obtain the result.

If \(k > 2\), then we reason similarly, with \(k\) events \(e_i\) and nodes \(n_i\), where each path cannot execute any actions from \(p\) without leading to a contradiction. We then conclude that the case (1) of Definition D.5 must apply for each path and we finally reach a pair of nodes in \(\ln(\hat{n}, e_1, e_2)\), as above.

Second, having established (3), we show that

\[
\forall (q, \ell_1, q_1) \neq (q, \ell_2, q_2) \in \delta_p : (q, \ell_1, q_1) \sim (q, \ell_2, q_2)\] (5)

We show this by contradiction, if (3) does not hold, we must have partitions of outgoing transitions from \(q\) such that the result hold within each partition, but not across two partitions, i.e., we have a situation of the form below, where \(n\) is the last node such that all
transitions of p from q are taken.

However, between two partitions either (i) case 1 of Definition 3.5 applies, i.e., the branches do not commute nor merge (cf. right-hand side of the diagram above) and thus, there must be a node n and two events e₁ and e₂ which will be checked for, by Lemma D.3, or (ii) case 2 of Definition 3.5 applies (cf. left-hand side of the diagram), in which case there is a two other nodes n₁ and n₂, which commute (or merge, cf., item (ii) in 2α of Definition 3.5) to a third node, n₃, and we can inductively repeat the argument in (i) where n₁ and n₂ replace n. Note that it must be the case that p executes some actions from nodes n₃, thus linking two partitions together.

D.4 Proof of Theorem 3.1 (Soundness)

Theorem 3.1 (Soundness). If S is GMC, then it is safe (no orphan message, deadlock, and unspecified reception configurations).

Proof. Absence of orphan message configuration and unspecified reception configurations follow directly from Lemma D.12.

We show absence of deadlock configuration, by contradiction, using Lemma D.13. Assume there is a deadlocking configuration s ∈ RS(S) such that s = (qₘ, ŝ). Let qₘ ∈ δₚ for some p ∈ P. By Lemma D.13, we have nₖ = n, and since S is representable, there must be an execution in which p ∈ δₚ is taken. Moreover, by Lemma D.12, there is no unspecification configuration, so it must be the case that there is a branching in Mₛ such that the machine is executing a message a in one branch, but not in the other (reasoning in a similar fashion to the proof of Lemma D.12). The branching in Mₛ must be reflected in TS(S) such that there is a branch going to n and another branch where the interaction on a takes place. However, in the n branch, the behavior of ŝ is empty (deadlock) and thus we obtain a contradiction with Definition 3.5 since ŝ executes p ∈ δₛ in one branch and does nothing (i.e., ε) in the other branch.

Lemma D.9. Let S = (Mp)∈P be a GMC system and TS(S) = (Nₚ, n₀ₚ, ŝₚ, ε). For all p ∈ P and for all q ∈ Qₚ, if (q, εₕ, q₁) ∈ (q, q₂, qₚ) ∈ δₚ (with εₕ ≠ ε₂), and there is n ∈ Nₚ whose q[n] = q, such that n ≈ with e₁ₚ = εₕ, then (q, εₕ, q₁) ≈ (q₂, qₚ).

Proof. Direct from Definition D.1 and Lemma D.5.

Lemma D.10. Let S = (Mp)∈P be a GMC system and TS(S) = (Nₚ, n₀ₚ, ŝₚ, ε). For all p ∈ P and for all q ∈ Qₚ, if (q, εₕ, q₁) ∈ δₚ (with εₕ ≠ ε₂), such that εₕ (resp. ε₂) is a send action (resp. receive action) and (q, εₕ, q₁) ≈ (q₂, qₚ), then there are n ∈ Nₚ, n[n] = q, such that n ≈ with e₁ₚ = εₕ.

Proof. By contradiction, assume there is no such n ∈ N. Then for all n such n ≈ with e₁ₚ = ε and e₁ₚ = εₕ, εₕ > 0. Observe that such nodes exist since (q, εₕ, q₁) ≈ (q₂, qₚ, qₚ). This implies that case 1 of Definition 3.5 cannot apply, and case 2 of Definition 3.5 cannot apply either (since there is no unique sender); which contradicts the fact that S is GMC.

Lemma D.11 (Mixed choice). Let S = (Mp)∈P be a GMC system, for all p ∈ P and for all q ∈ Qₚ such that (q, εₕ, q₁), (q, q₂, qₚ) ∈ δₚ for some q, qₚ ∈ Qₚ, if εₕ is a send action and ε₂ is a receive action, then there is q ∈ Qₚ such that (q, ε₂, qₚ) ∈ δₚ and (q₂, e, qₚ) ∈ δₚ.

Proof. By Lemma D.8, we must have (q, εₕ, q₁) ≈ (q, q₂, qₚ).

If (q, εₕ, q₁) ≈ (q, q₂, qₚ), the result follows directly by Lemma D.10 if (q, q₂, qₚ) = (q, ε₂, qₚ), then there must be a chain of transitions (q, ε₂, qₚ) such that

(q, ε₂, qₚ) ≈ ... ≈ (q, ε₂, qₚ)

where εₕ = ε, qₚ = q₂, ε₂ = ε₂, and qₚ = qₚ. We show that for all 1 ≤ i ≤ j ≤ k: qᵢ is a send action and qⱼ is a receive action, then there is q ∈ Qₚ such that (q, qᵢ, qₚ) ∈ δₚ and (qᵢ, qⱼ, qₚ) ∈ δₚ. We show the result by induction on the length k of the smallest chain of transitions for which (6) holds. Assume k = 3, then there are two cases

• If qₖ is a send action, then it must commute with q₃, by Lemma D.10. By Lemma D.9, we also have (q, q₁, q₂) ≈ (q, q₃, q₄), and we are done.

• If q₅ is a receive action, then it must commute with q₆, by Lemma D.10 and we reason as above.

The inductive case follows straightforwardly. Assume the result holds for k > 3 and let us show it holds for k + 1. If qₖ is a send action, it must commute with qⱼ+₁, which implies that there is a smaller chain ≈–linking qⱼ+₁ with qₖ (as above). If qₖ is a receive action, then it must commute with all the send actions qⱼ such that i < k, thus there is a smaller chain ≈–linking qⱼ+₁ with a send action, and we are done by induction hypothesis.

Lemma D.12. Let S = (Mp)∈P be a GMC system for all s ∈ RS(S), if s ∈ s₁s₂s₃s₄, with s₁a₁s₂a₂, then s₁s₂s₃s₄.

Proof. By contradiction, assume that there is s ∈ RS(S) such that s₁s₂s₃s₄ and for all ϕ such that s₁s₂s₃, we have s₁a₁s₂ϕ. In addition, assume that s is the first configuration (from s₀) such that a message cannot be received.

Assume that the state of a in s is qₚ. Since S is representable, there must be s₁ ∈ RS(S), where the state of a in s₁ is qₚ, such that s₁ ≈ a₁s₂s₃s₄, i.e., there is an execution where the message a sent by a is received by a₁. If the message cannot be received by a₁ from s, it means that either

1. a₁ is unable to fire the action a₁s₂s₃ because it is expecting another message, i.e., it is blocked by a transition pr₁b, or
2. there is a branching in Mₛ such that in one of the branch a is received but not in the other.

Case 1 implies that there is another configuration before s where a message is not received, thus we only consider case 2.

Let us consider the last state qₚ ∈ Qₚ such that (q₁, qₚ, qₚ), (q₂, qₚ) ∈ δₚ (ℓ₁ ≠ ℓ₂) and there is no a₁s₂s₃ transitions fireable from q₁ (before any other transition receiving from a) and the first transition receiving from a after q₂ is a₁s₄. Observe that since q is the last state where such a branching occur, there cannot be qₚ ∈ Q₂ such that (q₁, q₂, qₚ), (q₂, ℓ₁, qₚ) ∈ δ₂.
Essentially, we have the following situation:

\[ \text{\( x \) cannot execute \( x' a \) once it has taken the transition \( \ell_2 \).} \]

We have the following cases:

1. \( \ell_1 \) is a send action and \( \ell_2 \) is a receive action.
2. both \( \ell_1 \) and \( \ell_2 \) are send actions, or
3. both \( \ell_1 \) and \( \ell_2 \) are receive actions.

Following Lemma 3.11, case (i) cannot happen since it would imply that the branches commute.

Observe that if there are more than two transitions outgoing \( q \), we can partition them into two sets: (i) the transitions after which \( a \) is a received and (ii) the transitions after which \( a \) is not received.

By Lemma D.8, there must be, at least, one transition \( (q, \ell_2, q_2) \) in (i) and one transition \( (q, \ell_1, q_1) \) in (ii) such that \( (q, \ell_1, q_1) \sim (q, \ell_2, q_2) \). Hereafter, we only consider these two transitions. Case (i) of Definition 3.5 must apply to a branching node \( n \) such that \( n[q] = q \) and the first action of \( \pi \) in one branch is \( \ell_1 \) and the first action of \( \pi \) in the other branch is \( \ell_2 \); by definition of \( \sim \). Note that case (ii) of Definition 3.5 cannot apply for \( \pi \) since it would otherwise mean that there is a later branching in machine \( \pi \) such that it can "choose" whether to receive \( a \) or not; i.e., \( \pi \) must be involved in the choice.

Case (ii). Then, \( \pi \) is the selector at node \( n \). Following Definition 3.5, there are two sub-cases: Machine \( \pi \) executes a "receive action before sending another action. This implies that there must be a branching in \( \pi \) before the transition \( x' a \), which means that \( x' a \) can only be executed once the \( \ell_2 \)-branch has been executed. Thus, \( \pi \) cannot make a "bad choice": the two branches are mutually exclusive in all possible executions.

Indeed, if \( \pi \) was able to make another choice, it would mean that it could choose between receiving \( a \) and \( \ell_2 \), i.e., there is no \( \prec \)-dependency between \( \ell_1 \) and \( \ell_2 \). More formally, assume we have \( (q_1, x_1, \ell_2, q_2) \in \Delta_{x_1} \). By representability, there must be a node \( n \in N \) such that, for \( i \in \{1, 2\} \), \( n \equiv n_i \equiv \epsilon \) with \( e_i \equiv e_{x_i} \equiv e_{x_1} \equiv e_\epsilon \equiv \epsilon \), and \( n[q_1] = q_1 \). Also, by GMC and the fact that we assumed that the non-reception at \( \pi \) is the first one in the system, \( \pi \) must be able to receive both messages from \( s_1 \) and \( s_2 \) in each branch, i.e., we must have, for \( i \neq j \in \{1, 2\} \), \( n \equiv n_i \equiv n_j \equiv \epsilon \) with \( e_i \equiv e_j = e_{x_i} \equiv e_{x_1} \equiv e_\epsilon \equiv \epsilon \). If \( s_1 \equiv s_2 \), the two actions are always mutually exclusive (since the channels are message order preserving) and thus there is always a trivial \( \prec \)-dependency. If \( s_1 \neq s_2 \), we must have a \( \prec \)-chain between the two corresponding interactions in both branches. The shortest of such chains is as follows, with \( i \neq j \in \{1, 2\} \): \( s_1 \rightarrow \pi: \ell_i \not\prec \pi: \ell_j \)

which implies that \( \pi \) cannot choose between receiving either message in any asynchronous execution.

Case (iii). This means that another machine selects the branch:

- If \( \pi \) is the selector, then there must be a branching in machine \( \pi \) such that \( \pi \) sends a different message to all participants involved in the choice (possibly through other participants), including \( \pi \). This implies that \( \pi \) must be aware of which branch \( \pi \) has chosen and thus cannot make a "bad choice" either.
- If \( \pi \) is not the selector, there must be another participant making the decision and we reason as in case (ii) above.

D.5 Proof of Reachability

The following lemma states the reachability property, which is important to apply Cortell’s algorithm.

Lemma D.1. If \( S \) is GMC and \( TS(S) = (N, n_0, \hat{\ell}, \Rightarrow) \), then \( \forall n \in N : n_0 \Rightarrow^* n \).

Proof. By contradiction, assume there is \((\hat{q}, \hat{\ell}) \in RS_1(S)\) such that \( n_0 \Rightarrow^* \hat{q} \) and \((\hat{q}, \hat{\ell}) \in RS_1(S)\) such that \((\hat{q}, \hat{\ell})\) is reachable by 1-bounded execution from \((\hat{q}, \hat{\ell})\), but not by a synchronous execution. This implies that there is a dependency chain between the two configurations. The smallest such chain is:

\[ (\hat{q}, \hat{\ell}) \xrightarrow{sr} r \xrightarrow{\ell_1} \hat{q} \xrightarrow{\ell_2} \hat{q} \]

i.e., it is not possible to swap actions \( r \xrightarrow{\ell_1} \) and \( \ell_2 \), thus there is no synchronous counterpart for this execution. Such an execution implies that we have machines of the form:

\[ s : q_1 \xrightarrow{sr} q_2 \xrightarrow{r} q_3 \quad r : q_1 \xrightarrow{\ell_1} q_2 \xrightarrow{\ell_2} q_3 \]

By representability, each execution must be represented in \( TS(S) \), thus, there must be two branches in each machine, so that both branches appear in \( TS(S) \):

\[ q_1 \xrightarrow{sr} q_2 \xrightarrow{\ell_1} q_3 \]

\[ q_1 \xrightarrow{\ell_1} q_2 \xrightarrow{sr} q_3 \]

By assumption, branches \( A_s \) and \( A_r \) can be executed synchronously, since \( n_0 \Rightarrow^* \hat{q} \), while, by representability, \( A_s \) and \( B_s \) (resp. \( B_r \) and \( A_r \)) must be executable synchronously.

- If \( q_1 = q_4 \) or \( q_1' = q_4' \), we obtain a contradiction with the assumption that \((\hat{q}, \hat{\ell})\) is not reachable by a synchronous execution.
- Hence, if \( q_1 \neq q_4 \) and \( q_1' \neq q_4' \), there are three branches in \( TS(S) \): \((A_s, A_r)\), \((B_s, B_r)\), and \((B_r, A_r)\). This implies that machine \( \pi \) (resp. \( \pi \)) has the same first actions in branches \((A_s, A_r)\) and \((A_s, B_r)\) (resp. \((A_r, A_r)\) and \((B_s, A_r)\)). Thus, either the branches commute, i.e., \( q_1 = q_4 \), \( q_1 = q_4' \), \( q_1' = q_4' \), \( q_1' = q_4' \), or there is a path in \( \pi \) (resp. \( \pi \)) such that both branches merge, either way, this contradicts the fact that \((\hat{q}, \hat{\ell})\) is not reachable by a synchronous execution. If the branches do not commute nor merge, then the system is not GMC, which contradicts our assumptions.

Lemma D.13. If \( S \) is GMC and \( TS(S) = (N, n_0, \hat{\ell}, \Rightarrow) \), then \( \forall (\hat{q}, \hat{\ell}) \in RS(S) : n_0 \Rightarrow^* \hat{q} \).
In this section, we fix $D.6$ Complexity Analysis: Proofs of Propositions 3.1 and 3.2

**Proof.** The proof is similar to the proof of Lemma 4.1. By contradiction, assume there is $s \in R S_3(S) \cap R S_1(S)$ such that $s = (\vec{q}, \vec{e})$ and $\neg (\eta_0 \Rightarrow \vec{q})$. Then, there should be an execution of the form

$$s_0 \overset{ar'ia_1}{\rightarrow} s \overset{ar'ib}{\rightarrow} \ldots \overset{ar'ic}{\rightarrow} s_{1,1}$$

with $s_1 \in R S_1(S)$, stable, $s_2 \in R S_1(S)$ and $s_3 \in R S_2(S) \cap R S_1(S)$, such that there is a dependency chain in $\eta_0$ between $ar'ib$ and $ar'ic$ (otherwise, $s_1$ would be reachable by a 1-bounded execution). The smallest such chain is of the form $ra'l'c \cdot rs'c$. By representability, as in the proof of the lemma above, there must be two branches in each machine so that each trace can be executed synchronously, i.e.,

$$\begin{align*}
A_1 & \vdash q_1 \overset{ar'ia_1}{\rightarrow} q_2 \overset{ar'ib}{\rightarrow} q_3 \overset{ar'ic}{\rightarrow} q_4 \\
B_1 & \vdash q_5 \overset{ra'l'c}{\rightarrow} q_6 \overset{rs'c}{\rightarrow} q_7 \overset{\cdot}{\rightarrow} q_8
\end{align*}$$

Branches $A_2$ and $A_3$ can be executed synchronously, since $s_1$ is stable and $s_2 \in R S_1(S)$ (cf. Lemma 4.1). By representability, $A_1$ and $B_1$ (resp. $B_2$ and $A_2$) must be executable synchronously. Hence, if $q_1 \neq q_5$ and $q'_1 \neq q'_5$, there are three branches in $T S(S)_1$: $(A_{a_1} A_{a_2})$, $(A_{a_1} B_{a_2})$, and $(B_{a_1} A_{a_2})$. This implies that machine $a_1$ (resp. $a_2$) has the same first actions in branches $(A_{a_1} A_{a_2})$ and $(A_{a_1} B_{a_2})$ (resp. $(A_{a_1} B_{a_2})$ and $(B_{a_1} A_{a_2})$). Thus, either the branches commute, i.e., $q_1 = q_5$, $q_4 = q_8$, $q'_1 = q'_5$, $q'_4 = q'_8$, or they merge, which contradicts the fact that $(\vec{q}, \vec{e})$ is not reachable by a synchronous execution; or the system is not GMC, which contradicts our assumptions. \(\square\)

### D.6 Complexity Analysis: Proofs of Propositions 3.1 and 3.2

In this section, we fix $S = (M_p)_{p \in \mathcal{P}}, T S(S) = (N, n_0, \vec{E}, \Rightarrow)$, and we let $\delta_N$ be the biggest set of transitions of all the machines.

We first describe the maximum size of the basic constructions.

- $|N| = \prod_{p \in \mathcal{P}} |Q_p|$ since each node in $N$ consists of a $|\mathcal{P}|$ vector of states, each in $Q_p$.
- $|\delta_N| = |E| = 2|\mathcal{A}| \times |N| = |E|$ since there can be at most $2|\mathcal{A}|$ transitions between two nodes in $N$; indeed:
  - the local state components of each event is determined by the node it is fired from;
  - by construction, given two nodes, all the transitions between these nodes must involve the same machines $a$ and $r$;
  - the machines are deterministic, thus they can only send a same message once per state, and
  - each message may be sent by each one of the two machines.
- $|T S(S)| = |N| + |\delta_N|$
- $|\vec{E}| = |E|$, since, in the worst case, $\Rightarrow$ is the identity relation.
- Relation $\prec$ can be computed via a breadth-first traversal of $T S(S)$, i.e., in $O(|N| + |\vec{E}|)$ time, and its maximal size if $|N|^2$.

Note that it is not necessary to compute the transitive closure of $\prec$ since, to check whether a node is a “last node”, it suffices to check its (direct) neighbours.

**Proposition D.1.** The $\bowtie$-relation is computable in time

$$O\left(\sum_{p \in \mathcal{P}} \left(\delta_p^8\right) + \left(|E| \times (2 \times |\delta_N|^2)\right)\right)$$

**Proof.** First, we observe that computing the $\bullet$-relation for each machine can be done in time $O\left(\delta_p^8\right)$ since the size of the non-transitive version of $\bullet$ is $\delta_p^2$ and computing the transitive closure of a binary relation has a cubic complexity. Then, to compute the $\bowtie$-relation, for each pair of events in $E$, we have to look up in two sets of $\bullet$ relations.

**Proposition 3.1.** Given a system $S = (M_p)_{p \in \mathcal{P}}$, checking whether $S$ satisfies the representability condition is computable in

$$O\left(\sum_{p \in \mathcal{P}} 2^{|N| + |Q_p|} \right) \text{ time, with } |N| = \prod_{p \in \mathcal{P}} |Q_p|$$

**Proof.** The first part of the definition requires, for each machine, to (i) compute its projection from $T S(S)$ (which depends on the size of $N$) and (ii) to check that this projection is language equivalent with the original machine. The second part requires for each machine (sum over $\mathcal{P}$), for each state ($Q_p$ factor), and for each transition from that state ($\delta_p$ factor) to look for a node in $T S(S)$ ($N$ factor) such that this transition is reflected in $T S(S)$ ($\delta$ factor). Since for each local state, we are only interested in the nodes where this local state appears, we divide by $|Q_p|$. Hence, we have that the time complexity of the representability condition is:

$$O\left(\sum_{p \in \mathcal{P}} \left(\delta_p^8 + 2^{|N| + |Q_p|}\right) + \sum_{p \in \mathcal{P}} \left(|Q_p \times \delta_p \times N| \times \left(\delta_p^2\right)\right)\right) = O\left(\sum_{p \in \mathcal{P}} \left(\delta_p^8 + 2^{|N| + |Q_p|}\right) + \sum_{p \in \mathcal{P}} \left(|Q_p \times N \times \delta_p^8\right)\right) = O\left(\sum_{p \in \mathcal{P}} \left(2^{|N| + |Q_p|}\right)\right)$$

**Proposition 3.2.** Given a system $S = (M_p)_{p \in \mathcal{P}}$, checking whether $S$ satisfies the branching property is computable in

$$O\left(|\Rightarrow|^2 \times |\Rightarrow| \times \sum_{p \in \mathcal{P}} \left(\delta_p^8\right)\right) \text{ time.}$$

**Proof.** The check of part (1) of Definition 3.3 is $O\left(|\Rightarrow|^2 \times |\Rightarrow|\right)$, so we consider the most complex case, namely part (2) of Definition 3.3, which is the sum of the complexity of conditions $\left(2a\right)$, $\left(2b\right)$, and $\left(2c\right)$ of Definition 3.3.

For each branch with events $e_1 \neq e_2$ and each participant $p \in \mathcal{P}$, we have to compute the sets $I_p^L$, then $I_p^E$. This also allows to compute the last nodes and can be done considering simple paths only, and is easily computed with a breadth-first visit of $T S(S)$, and therefore this is done in polynomial time in $T S(S)$.

Once this is done it is easy to see that the check of part (2b) of Definition 3.3 is polynomial in $|\mathcal{P} \cup Act|$. 

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The most complex part of the computation is to check conditions (2b)(ii) and (2c) because it requires to check the paths in TS and the enumeration of all paths is computed in O(|\(\vec{\delta}\)|^2).

Finally, the check of condition (2b)(ii) can be done in O\((2 \times |TS(S)|)\)
while (2c) is checked in O\(|\vec{\delta}|^2 \times |\vec{\delta}|! \times \sum_{\vec{\delta} \in \delta} (|f|^2)\) time.

E. Proofs of Section 4

E.1 Equivalences

Lemma B.1. If T is the reachability graph of the Petri net N obtained from TS via the algorithm in [19], and T' is the reachability graph of the Petri net obtained after applying Transformation B.1, then T \(\cong\) T'.

Proof. Trivial since N is safe and the only added transition is labelled by \(\epsilon\).

Lemma B.2. If T (resp. T') is the reachability graph of the Petri net N obtained after Transformation B.1 (resp. Transformation B.2), then T \(\cong\) T'.

Proof. Follows from the fact that N is safe and the only added transitions are labelled by \(\epsilon\).

Theorem 4.1 (Completeness). Given a GMC system S = (M)_{p \in \mathbb{P}} - let G be the global graph built from S and let TS(S) = (N, \(n_0\), \(E\), \(\rightarrow\)). If TS(S) is self-loop free (i.e. \(\forall n, n' \in N : n \rightarrow n' \Rightarrow n \neq n'\)), then S is isomorphic to (G)_{p \in \mathbb{P}}, the system made of the projection of G.

Proof. The proof follows from (1) the assumption that TS(S) is self-loop free, (2) Lemmas B.1 and B.2, and (3) the fact that transformation to (pre-) global graph preserves the structure and labels of the joined net.

E.2 Complexity Analysis of the Transformations

Proposition B.1. Transformation B.1 is computable in linear time in the size of \(m_0\).

Proof. Trivial.

Proposition B.2. Transformation B.2 is computable in polynomial time in the size of \(N\).

Proof. The algorithm for part (1) of the transformation works as follows: (1) compute the preset of each place (\(|F|\)), (2) sort a table of pairs (preset, place) by preset, e.g., a lexicographic order on sets of transitions (\(|P| (\log |P|))\), (3) go through the table and apply the transformation on each set of places which have the same preset (\(|P|\)). Observe that, once a set of places with the same preset has been identified, the transformation can be done in linear time. The algorithm for part (2) works similarly.

Proposition B.3. Transformation B.3 is computable in polynomial time in the size of \(N\).

Proof. One has to iterate on each element in \(P \cup T \cup F\) and the composition of graphs is polynomial in the size of the two graphs (number of vertices).

Proposition B.4. Transformation B.4 is computable in polynomial time in the size of \(N\).
F. Benchmark Protocols
In this section, we give a graphical representation of each protocol used in the benchmark. Textual representations of these are also available in [6] (cf. gmc-synthesis/tests/benchmark/gmc directory).

F.1 Running Example
This is our running example.
F.2 Bargain Protocol
This is a simple example of bargaining protocol, where machine 0 is a client, machine 1 is seller, and machine 2 is a bank.
F.3 Alternating 3-bit protocol

This protocol, adapted from [20], models a protocol where machine 0 repeatedly sends to machine 1 alternating messages $m_1$, $m_2$, and $m_3$ but will always concurrently wait for the acknowledgement $a_i$ before sending $m_i$. 
F.4 Alternating 2-bit protocol
This protocol is adapted from [20], this is the 2-message version of the above protocol.
F.5 TPM Contract v2
This protocol models a Singularity channel contract, it the minimised version of the corrected contract proposed in [23].
F.6 Sanitary Agency

This protocol, adapted from [31], models a software system that aims at “supporting elderly citizens in receiving sanitary assistance from the public administration”. In our formalisation, machine 0 is the Citizen, machine 1 is the Sanitary Agency, machine 2 is the Coop, and machine 3 is the Bank.
F.7 Health System

This protocol is adapted from [15], where machine 0 is HS, machine 1 is P, machine 2 is SS, machine 3 is AS, machine 4 is T, and machine 5 is ES.
F.8 Filter Collaboration
This protocol is adapted from [33].
F.9 Logistic

This protocol is adapted from [1], it is one of the example given in the reference for BPMN Choreography, where machine 0 is Supplier, machine 1 is Retailer, machine 2 is Consignee, and machine 3 is Shipper.
F.10  CloudSystem v4

This protocol is adapted from [22], machine 0 is CL, machine 1 is APPLI, machine 2 is INT, and machine 3 is DB.